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<td>Author(s)</td>
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MULTIPLIER-LESS DISCRETE SINUSOIDAL AND LAPPED TRANSFORMS USING SUM-OF-POWERS-OF-TWO (SOPOT) COEFFICIENTS

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Abstract

This paper proposes a new family of multiplier-less discrete cosine and sine transforms called the SOPOT DCTs and DSTs. The fast algorithm of Wang [10] is used to parameterize all the DCTs and DSTs in terms of certain (2x2) matrices, which are then converted to SOPOT representation using a method previously reported by [6, 7]. The forward and inverse transforms can be implemented with the same set of SOPOT coefficients. A random search algorithm is also proposed to search for these SOPOT coefficients. Experimental results show that the (2x2) basic matrix can be implemented, on the average, in 6 to 12 additions. The proposed algorithms therefore require only O(N log, N) additions, which is very attractive for VLSI implementation. Using these SOPOT DCTs/DSTs, a family of SOPOT Lapped Transforms (LT) is also developed. They have similar coding gains but much lower complexity than their real-valued counterparts.

I. INTRODUCTION

Discrete Cosine and Sine transforms (DCTs and DSTs) are frequently used in signal and image processing applications. For example, the DCT-II, which is usually referred to as the DCT, is commonly used in image, video and audio coding. The four types of DCTs (DSTs) are also useful in implementing the lapped transforms [4, 5] and the cosine modulated filter banks (CMFB) [1-3]. Recently, there is a considerable interest in designing filter banks with low arithmetic complexity. Filter banks using integer coefficients [1], and the sum-of-powers-of-two (SOPOT) representation [6, 7] were proposed. One important problem with integer CMFB and integer lapped transform [8] is that it is very difficult to design orthogonal integer matrices which resemble the various sinusoidal transforms when the size of that transform increases. Up to now, only integer DCT-IV up to order 8 has been reported. In order to overcome this difficulty, the authors have recently proposed a multiplier-less DCT-IV for the implementation of the conventional CMFB [7], which is based on the SOPOT representation. Multiplier-less DCT-IV up to 1024 and higher can be designed. In this paper, we further generalize this technique to cover the four types of DSTs and DCTs. The fast decomposition algorithm of Wang [10] is used to parameterize all the DCTs and DSTs in terms of a set of (2x2) matrices. These matrices, which are closely related to the (2x2) rotation matrix, are then converted into SOPOT representation using the method we have introduced in [7]. This allows us to implement both the forward and inverse transforms with the same set of SOPOT coefficients. Moreover, as the proposed SOPOT DCTs and DSTs are derived from the fast algorithms of Wang [10], it only requires O(N log, N) additions and the implementation of O(N log, N) (2x2) basic matrices as mentioned earlier, where N is the length of the transforms. As each (2x2) basic matrix can be implemented, on the average, in 6 to 12 additions, the proposed multiplier-less transforms require only O(N log, N) additions, which is very attractive for VLSI implementation. Using these SOPOT DCTs/DSTs, a family of SOPOT Lapped Transforms is also developed. They have similar coding gains but lower implementation complexity than their real-valued counterparts. It should be noted that another (8x8) multiplier-less DCT-II, called binDCT, can also be obtained from the Gauss-Jordan factorization and the lifting structure [9]. Our approach differs from [9] in that it is based on the fast DCT/DST algorithms of Wang which can be generalized to different types of sinusoidal transforms with different transform lengths. Moreover, the parameterization is based on a rotation-like (2x2) matrix, which is expected to have better numerical properties, especially when the transform size is large. The approach described here can also be generalized to design SOPOT approximation to discrete Fourier transform (DFT) and discrete Hartley transform (DHT) [15].

This paper is organized as follows: Sections II and III are devoted to the definition of the four types of DCTs and DSTs, and their fast algorithms. The proposed SOPOT DCTs and DSTs are discussed in Section IV followed by several design examples in Section V. Finally, conclusions are drawn in section VI.

II. THE SINUSOIDAL TRANSFORMS: DCT AND DST

According to Wang [10], there are four different types of discrete cosine transforms (DCT) and discrete sine transforms (DST). These transforms have been developed at various times by Ahmed et al [11], Kitajima [12], Jain [13] and Kekre and Solanki [14]. The definitions of the DCTs and DSTs are given as follows,

A. Four types of DCT Matrices

\[
[C_{n+1,k+1}^a] = \sqrt{2/N} \{ \cos(k\pi/n) \}, \\
[k,n = 0,1,\ldots,N].
\]

\[
[C_{n+1,k+1}^b] = \sqrt{2/N} \{ \cos(k\pi/2) \}, \\
[k,n = 0,1,\ldots,N-1].
\]

\[
[C_{n+1,k+1}^c] = \sqrt{2/N} \{ \cos((k+1/2)\pi/n) \}, \\
[k,n = 0,1,\ldots,N-1].
\]

\[
[C_{n+1,k+1}^d] = \sqrt{2/N} \{ \cos((k+1/2)(n+1/2)\pi/n) \}, \\
[k,n = 0,1,\ldots,N-1].
\]

B. Four types of DST Matrices

\[
[S_{n+1,k+1}^a] = \sqrt{2/N} \{ \sin(k\pi/n) \}, \\
[k,n = 1,2,\ldots,N-1].
\]

\[
[S_{n+1,k+1}^b] = \sqrt{2/N} \{ \sin((k+1/2)\pi/n) \}, \\
[k,n = 1,2,\ldots,N].
\]

\[
[S_{n+1,k+1}^c] = \sqrt{2/N} \{ \sin((k-1/2)\pi/n) \}, \\
[k,n = 1,2,\ldots,N-1].
\]

\[
[S_{n+1,k+1}^d] = \sqrt{2/N} \{ \sin((k+1/2)(n+1/2)\pi/n) \}, \\
[k,n = 0,1,\ldots,N-1].
\]

\(e_i\) is equal to \(1/\sqrt{2}\) for \(i = 0, N \) and 1 otherwise. The superscripts and subscripts represent respectively the type and the size of the transforms. Also, let \(X_k^n(k)\), \(K=I\) to \(IV\), and \(x(n)\) be the type \(K\) cosine transformed and the input sequences, respectively. For simplicity, it is assumed that the scaling factors are absorbed into \(X_k^n(k)\) or \(x(n)\) so that \(e_i\) and \(e_s\) are taken as unity in the subsequent development. We now summarize the decompositions for the various DCTs and DSTs to construct their multiplier-less transformati...
III. DECOMPOSITION OF DCTs and DSTs

DCT-I

From the definition of DCT-I [10], it can be shown that, for an even number, its even and odd parts can be decomposed as

\[ X_n^{(m)}(2k) = \sum_{m=0}^{(N/2)-1} X(n) \cos \left( \frac{mk}{N/2} \right) \left( (-1)^k \right) x(N/2)_n, \]

\[ X_n^{(m)}(2k+1) = \sum_{m=0}^{(N/2)-1} X(n) \sin \left( \frac{mk}{N/2} \right) \left( (-1)^k \right) x(N/2)_n, \]

where \( k = 0, 1, \ldots, (N/2)-1 \).

Therefore, an DCT-I can be computed by a DCT-I and a DCT-III of lower order. (3-1) can be written more compactly as the following matrix representation

\[ C_n^{(m)} = P_n C_{n/2}^{(m)} \otimes C_{n/2}^{(m)}, \]

where \( P_n \) is the permutation matrix that accomplishes the permutation of the even and odd indexed parts.

DCT-II and DCT-III

Similarly, a DCT-II with an even length \( N \) can be decomposed into an \( (N/2) \)-point DCT-II and DCT-IV.

\[ X_n^{C_2}(2k) = \sum_{m=0}^{(N/2)-1} X(n) \cos \left( \frac{m(2k+1)}{N/2} \right), \]

\[ X_n^{C_2}(2k+1) = \sum_{m=0}^{(N/2)-1} X(n) \sin \left( \frac{m(2k+1)}{N/2} \right), \]

where \( k = 0, 1, \ldots, (N/2)-1 \).

In matrix notation, (3-3) reads

\[ C_n^{(m)} = P_n C_{n/2}^{(m)} \otimes C_{n/2}^{(m)} + B_n, \]

where \( P_n \) is the permutation matrix that accomplishes the permutation of the even and odd indexed parts and

\[ B_n = \begin{bmatrix} I_{n/2} & 0 \\ 0 & -I_{n/2} \end{bmatrix}. \]

The decomposition for DCT-III can be obtained by observing that DCT-III and DCT-II are inverses of each other. It then follows from (3-4) that

\[ C_n^{(m)} = C_n^{(m)} \otimes C_n^{(m)} + B_n, \]

DCT-IV

To obtain the decomposition for DCT-IV

\[ X_n^{C_4}(k) = \sum_{n=0}^{N-1} x(n) \cos \left( \frac{2\pi k}{4N} \right) \left( 2n \right) + \left( N-k \right), \]

Let's define the following sequences

\[ Y_n(k) = X_n^{C_4}(k) \cos \phi_n + X_n^{C_4}(N-k-1) \sin \phi_n, \]

\[ Y_n(N-k) = X_n^{C_4}(k) \sin \phi_n - X_n^{C_4}(N-k-1) \cos \phi_n, \]

where \( k = 0, 1, \ldots, N-1 \) and \( \phi_n = \pi(2k+1)/4N \). It can be shown [10] that \( X_n^{C_4}(k) \) can be expressed in terms of \( Y_n(k) \) as follows

\[ X_n^{C_4}(k) = Y_n(k) \cos \phi_n + Y_n(N-k-1) \sin \phi_n, \]

\[ X_n^{C_4}(N-k-1) = Y_n(k) \sin \phi_n - Y_n(N-k-1) \cos \phi_n, \]

where \( k = 0, 1, \ldots, N-1 \). \( Y_n(k) \) can be computed via DCT-III and DCT-IV of lower order as follows,

\[ Y_n(0) = x(0) + \sum_{k=1}^{N-1} \left[ x(2n-1) - x(2n) \cos \left( \frac{k}{2N} \right) \right], \]

or

\[ Y_n(N-k-1) = (-1)^k x(N-1) \]

with no additional summands. The order-\( N/2 \) DCT-III and DCT-IV can further be decomposed into DCT-IV of lower order. In matrix notation, we have

\[ C_n^{C_2} = T_n C_{n/2}^{C_2} \otimes C_{n/2}^{C_2}, \]

where \( T_n = \begin{bmatrix} I & 0 \\ 0 & -I \end{bmatrix}, \)

Similar decompositions can be derived for the DSTs, and are omitted here due to page limitation.

IV. MULTIPLIER-LESS DISCRETE SINUSOIDAL TRANSFORMS

The main difficulty in constructing a multiplier-less transformation is that the coefficients of the matrix transformation and its inverse cannot in general be expressed in terms of SOPOT coefficients. Let's consider the following simple matrix in (3-12)

\[ R_\theta = \begin{bmatrix} \cos \theta & \sin \theta \\ \sin \theta & -\cos \theta \end{bmatrix}. \]

If \( \cos \theta \) and \( \sin \theta \) are expressed directly in terms of SOPOT coefficients, say \( \alpha \) and \( \beta \). The inverse of \( R_\theta = \begin{bmatrix} \alpha & \beta \\ \beta & -\alpha \end{bmatrix} \) is

\[ R_\theta^t = \frac{1}{\sqrt{\alpha^2 + \beta^2}} \begin{bmatrix} \alpha & \beta \\ \beta & -\alpha \end{bmatrix}. \]

As \( \alpha \) and \( \beta \) are SOPOT coefficients, the term \( \frac{1}{\sqrt{\alpha^2 + \beta^2}} \) cannot in general be expressed as SOPOT coefficients. The basic idea of the proposed multiplier-less sinusoidal transforms is based on the following factorization of the matrix \( R_\theta \) and its inverse.

\[ R_\theta = \begin{bmatrix} \cos \theta & \sin \theta \\ \sin \theta & -\cos \theta \end{bmatrix} = \begin{bmatrix} 1 & \tan(\theta/2) \\ 0 & -1 \end{bmatrix} \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} \tan(\theta/2) & 0 \\ 0 & 1 \end{bmatrix}, \]

Since these factorizations of \( R_\theta \) and \( R_\theta^t \) involve the same set of SOPOT coefficients, i.e. \( \sin \theta \) and \( \tan(\theta/2) \), they can be directly quantized to SOPOT coefficients as follows

\[ R_\theta = \begin{bmatrix} 1 & \beta \theta \\ 0 & -1 \end{bmatrix} \begin{bmatrix} \alpha \theta & 0 \\ 0 & 1 \end{bmatrix} \]

where \( \alpha \theta \) and \( \beta \theta \) are respectively SOPOT approximations to \( \sin \theta \) and \( \tan(\theta/2) \). These coefficients are represented as

\[ \alpha \theta = \sum_{i=0}^{r} a_i \theta, \quad \beta \theta = \sum_{i=0}^{r} b_i \theta, \quad \alpha \theta, \beta \theta \in \{-1,1\}, \]

where the range of the coefficients and the number of terms being used in each coefficient. Replacing \( R_\theta \) in the fast sinusoidal transform algorithms in Section III, the desired multiplier-less sinusoidal transforms can be obtained. Length \( N = 2^n \) DCT-II, -III, -IV, and length \( N = 2^n + 1 \) DCT-I can be generated where \( m \) is a positive integer. Similar SOPOT DSTs can be generated. The remaining problem is to search for the SOPOT coefficients such that certain criteria are minimized subject to a given implementation complexity. For example, in signal coding applications, the coding gain of the transform can be used as the criterion for minimization. For multiplier-less digital filters
employing SOPOT coefficients, the total number of terms in the SOPOT coefficients is usually used as a measure of its implementation complexity. It can be seen that this is a combinatorial optimization problem. In this paper, a random search algorithm is used to perform this discrete optimization. More precisely, a random vector with all its elements bounded by \pm 1 is first multiplied by a scaling factor \( s \), and is added to the parameter vector containing the real-value of \( \alpha_a \) and \( \beta_a \). It is then quantized to the nearest SOPOT coefficients. The objective function is then evaluated for this SOPOT candidate. The one with the best performance at a given number of additions is recorded. The search continues until the maximum allowed number of trials is exceeded. The scale factor controls the size of the neighborhood to be searched. A number of solutions with different tradeoffs between implementation complexity and performance are then obtained. For the DCTs and DSTs, the mean squared error between the impulse responses of the candidate vector and the real-valued transform is used as the performance measurement. Also, when the number of channels, in the power of two, is more than about 64, all the transformation can be calculated using the early mentioned decomposition. The previous generated lower order DCTs/DSTs can be used to reduce the searching time.

V. DESIGN EXAMPLES

We now present some design examples for the DCTs and DSTs.

SOPOT DCT-I, II, III and IV

Table 1 shows the parameters of the proposed SOPOT DCT-I, II, III and IV with \( m = 4 \). It can be seen that the multiplication with the SOPOT coefficients \( \alpha_a \) and \( \beta_a \) can be implemented, on the average, as approximately 2 additions per coefficient. The frequency responses of an 8-channel SOPOT DCT-II and an 64-channel SOPOT DCT-IV are plotted in figure 2 and 3, respectively. The coding gain performance and the implementation complexity of the proposed SOPOT DCTs and their real-valued counterparts are further compared in table 2. It can be seen that the SOPOT DCTs have similar coding gains as their real-valued counterparts.

\[
G = \frac{1}{M} \sum_{i=1}^{M} \frac{E[A_i B_i]}{E[A_i]^2}
\]

where \( A_i = \sum_{j=0}^{N-1} h_i(j) h_i(j)^T \) and \( B_i = \frac{1}{M} \sum_{j=0}^{N-1} g_i(j) \).

In designing the SOPOT LT, we have used the coding gain as the performance measure.

VI. CONCLUSIONS

As mentioned earlier, one of the applications of the proposed SOPOT DCTs/DSTs is to implement multiplier-less lapped transform and CMFB. In this section, we shall present several design examples on SOPOT Lapped Transforms. Further results on the design of different types of SOPOT CMFB will be reported elsewhere. Figure 1 shows the general structure of an \( M \)-channel Lapped Transform (LT) with \( M = 8 \). The \( M \)-channel LT is an \( M \)-channel perfect reconstruction filter bank with filter length \( 2M \). The proposed SOPOT Lapped Transform (LT) is obtained by replacing the \( C_i^n \), \( C_i^{n+1} \), and \( S_i^{n+1} \) with their SOPOT counterparts obtained in the previous sub-section.

SOPOT LAPPEDED TRANSFORMS

In designing the SOPOT LT, we have used the coding gain as the performance measure.

\[
G = \frac{1}{M} \sum_{i=1}^{M} \frac{E[A_i B_i]}{E[A_i]^2}
\]

where \( A_i = \sum_{j=0}^{N-1} h_i(j) h_i(j)^T \) and \( B_i = \frac{1}{M} \sum_{j=0}^{N-1} g_i(j) \).

\( h_i(n) \) and \( g_i(n) \) are the impulse responses of the \( k \)-th analysis and synthesis filters of length \( N \). Here the input is assumed to be a first-order auto-regressive process with correlation coefficient \( \rho = 0.95 \).

The coding gains of Lapped Transform (LT) and the proposed SOPOT LT are given in table 3. It can be seen that the coding gains of the SOPOT LTs are very close to their real-valued counterparts. However, only about two additions are required to implement each multiplication in the SOPOT LT. As another illustration, the frequency response of an 8-channel SOPOT LT is given in Figure 4.

VI. CONCLUSIONS

A new family of multiplier-less discrete cosine and sine transforms called the SOPOT DCTs and DSTs is presented. They are derived from the fast algorithms of Wang [10] by parameterizing all the DCTs and DSTs in terms of certain (2x2) matrices. Using a method previously proposed by the authors [7],

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<th>Table 1a. Coefficients of the 17-channel SOPOT DCT-I</th>
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<td>( R_{04} )</td>
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<td>( R_{08} )</td>
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<td>( R_{16} )</td>
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<th>Table 1b. Coefficients of the 16-channel SOPOT DCT-II and DCT-III</th>
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<tr>
<td>( R )</td>
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<td>( R_{04} )</td>
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<th>Table 1c. Coefficients of the 16-channel SOPOT DCT-IV</th>
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<td>( R )</td>
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<td>( R_{04} )</td>
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<td>( R_{16} )</td>
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Figure 1. Flow graph of 8-channel LT.
these $(2 \times 2)$ matrices are then converted to SOPOT representation. The forward and inverse transforms can then be implemented with the same set of SOPOT coefficients. A random search algorithm is also proposed to search for these SOPOT coefficients. Experimental results show that the $(2 \times 2)$ matrix can be implemented, on the average, in 6 to 12 additions. The proposed algorithms therefore require only $O(N \log_2 N)$ additions, which is very attractive for VLSI implementation. Using these SOPOT DCTs/DSTs, a family of SOPOT Lapped Transforms (LT) is also developed. They have similar coding gains but much lower complexity than their real-valued counterparts.

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