

Developing a Voltage-Stability-Constrained Security Assessment System

Part I : Determination of Power System Voltage Security Operation Limits

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Abstract—The method for determining the voltage security operation limits in a practical voltage security analysis (VSA) system based on VSAT software for large power systems is introduced in this paper. These operation limits include bus voltage limits, branch/corridor transfer power limits and P-load limit of the whole system. The voltage security operation limits are determined by the most critical contingency among the studied contingency set. The most critical contingency determines the P-load limit of the whole system, and all kinds of operation parameter limits are operation parameter values corresponding to this P-load limit under pre-contingency. An operation parameter limit is upper limit if the function relationship between this operation parameter and load power is an increasing curve, or lower limit if the function relationship between this operation parameter and load power is an decreasing curve. These operation parameter limits are helpful for operators to monitor the system operation state.

Index Terms—voltage security analysis, operation limits, power systems

I. INTRODUCTION

THE market mechanism is introducing into power industry around the world. Power markets can improve the efficiency of power industry, but at the same time it will push power system operation state closer to its limit, which makes power system security analysis and monitoring system more and more important [1, 2]. This paper focuses on power system *voltage security analysis*.

This is the first part in a two-part paper on the development of a voltage-stability-constrained security assessment system (VSC-SAS), with emphasis on fundamental physical concept and how to determine system operation limits mainly about bus voltage and line or corridor transfer power with voltage-stable-constraints and system security considered. The main aim of the developed VSC-SAS is to provide system operation limits with respect to a usual contingency set. These operation limits include bus voltage limits, branch/corridor transfer power limits, active power loading limit of the whole system etc. In practical operation, real-time operation conditions from SCADA/EMS can be compared with these limits and the distance between them represents power system voltage-security-constrained margin.

The operation limits gotten by VSC-SAS can be visualized for operators to monitor power system operation state and know how far the current operation state is from voltage stability boundary. It is obvious that this kind of information is significant for the operator to avoid voltage instability and collapse. In addition to VSC, line power thermal limits and generator capacity limits etc. should also be considered. The security criteria can be N-1 or possible N-2 contingencies. The fundamental physical concept and calculation in determination of system operation limits is the key issue, which is presented below.

The overall structure of the developed VSC-SAS is as the following figure.

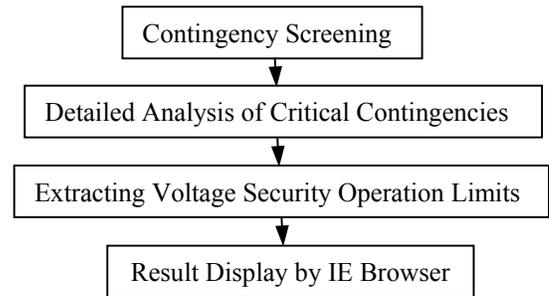


Fig. 1 Overall structure of developed VSA system

II. BUS VOLTAGE LIMITS

Suppose that there are two contingency A_1, A_2 , Fig. 2 shows PV-curves of bus j under pre-contingency (base case), contingency A_1 , and contingency A_2 . C_1 is the nose point of P-V curve for contingency A_1 . Contingency A_1 includes load shedding protection scheme while contingency A_2 doesn't. Note that the starting point of P-V curve for contingency A_1 is not the same as that of base case because of load shedding. With the increasing of P-load the operation point under base case moves towards T_1 . As shown in Fig. 2, T_1 becomes C_1 under contingency A_1 , and C_2 under contingency A_2 . It is easy to see that if the operation point beyond T_1 under base case it is not secure since if contingency A_1 occurs the post-contingency operation point doesn't exist. Contingency A_1 is

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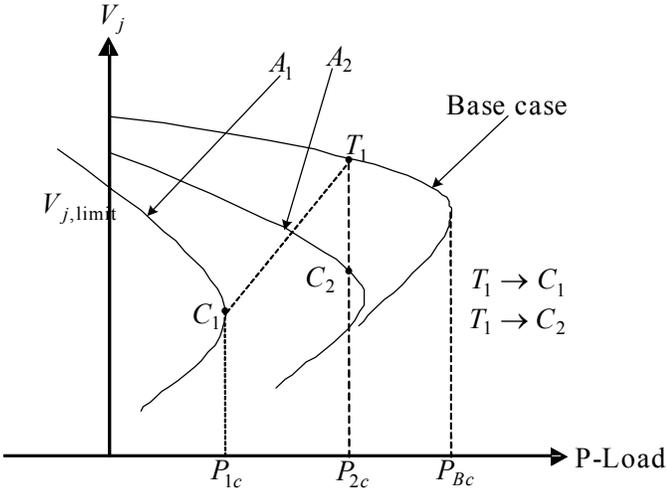


Fig. 2 Bus voltage limit determined by Contingency A_1

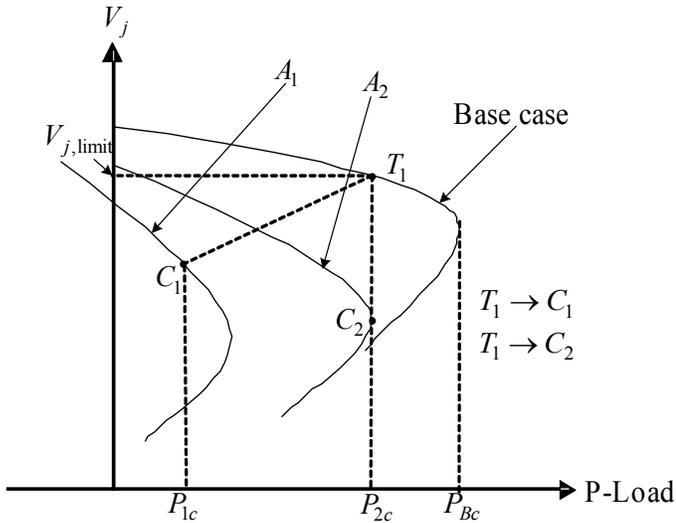


Fig. 3 Bus voltage limit determined by Contingency A_2

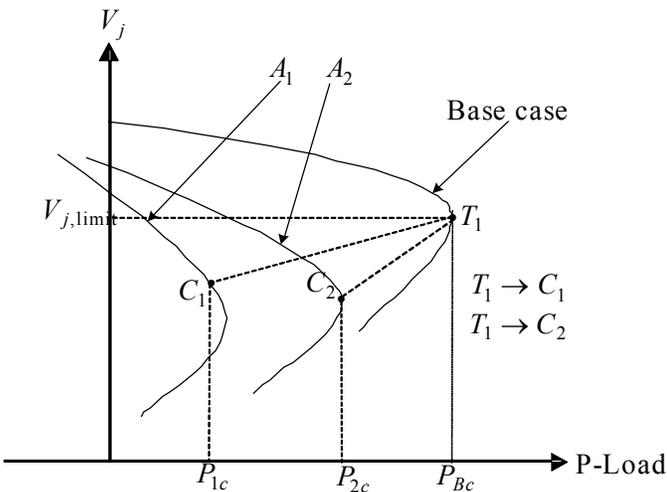


Fig. 4 Bus voltage limit determined by base case more critical than contingency A_2 . The voltage limit of bus

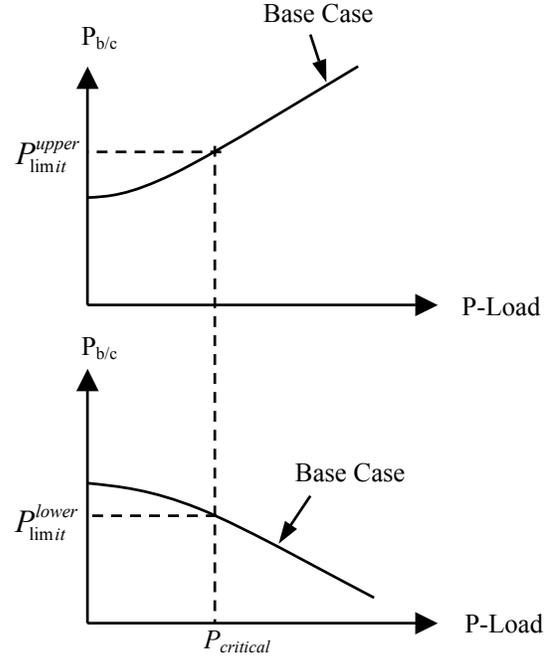


Fig. 5 Branch/corridor transfer power limits

j is the voltage value corresponding to T_1 under base case, which is determined by contingency A_1 . We call P-load value of T_1 *critical power*.

In the above case, the voltage limit of bus j is determined by contingency A_1 . Fig. 3 shows the situation where the voltage limit of bus j is determined by contingency A_2 . Fig. 4 shows the situation where the voltage limit of bus j is determined by base case.

III. BRANCH/CORRIDOR TRANSFER POWER LIMITS

On the curve branch/corridor transfer power – load power of branch/corridor j (PP curve), the transfer power limit for branch/corridor j is the branch/corridor transfer power value corresponding to the critical load power $P_{critical}$. See Fig. 5. Note that if the PP curve of branch/corridor j is an increasing curve the branch/corridor transfer power limit $P_{j,limit}$ we find is upper limit

$$P_{j,limit} = P_{limit}^{upper}$$

That is, $P_j < P_{j,limit}$ is considered as safe operating state under this situation. If the PP curve of branch/corridor j is a decreasing curve, the branch/corridor transfer power limit $P_{j,limit}$ we find is lower limit

$$P_{j,limit} = P_{limit}^{lower}$$

That is, $P_j > P_{j,limit}$ is considered as safe operating state under this situation.

IV. MATHEMATICAL DESCRIPTION OF PROCEDURE TO FIND ALL LIMITS

Suppose that the direction of load increasing is \vec{d} (increasing step vector), the initial value of load vector is \mathbf{P}_0 , load vector \mathbf{P} can be written as

$$\mathbf{P} = \mathbf{P}_0 + k \cdot \vec{d}, \quad k = 0, 1, 2, \dots \quad (1)$$

The load flow equation for contingency C_i can be written as

$$\mathbf{F}(\mathbf{P}_0 + k \cdot \vec{d}, \mathbf{V}_k^i, C_i) = 0, \quad i = 0, 1, 2, \dots, N \quad (2)$$

And so there are some inequality constraints such as maximum generation capacity, branch thermal capacity etc

$$\mathbf{G}(\mathbf{P}_0 + k \cdot \vec{d}, \mathbf{V}_k^i, C_i) \leq 0, \quad i = 0, 1, 2, \dots, N \quad (3)$$

where $i = 0$ represents pre-contingency (base case), i.e. with no contingency occurring. \mathbf{V}_k^i is the voltage vector after contingency C_i occurs at the load vector value $\mathbf{P}_0 + k \cdot \vec{d}$.

Definition 1: Critical Power \mathbf{P}_{cr} for voltage stability is defined as

$$\mathbf{P}_{cr} = \mathbf{P}_0 + K \cdot \vec{d} \quad (4)$$

where K is an integer satisfying that

i) For $\forall k \leq K, \forall i \leq N, \mathbf{F}(\mathbf{P}_0 + k \cdot \vec{d}, \mathbf{V}_k^i, C_i) = 0$ is solvable and $\mathbf{G}(\mathbf{P}_0 + k \cdot \vec{d}, \mathbf{V}_k^i, C_i) \leq 0$ is also satisfied;

ii) For $k = K + 1$, there at least exist one integer I such that $\mathbf{F}(\mathbf{P}_0 + k \cdot \vec{d}, \mathbf{V}_k^I, C_I) = 0$ cannot be solvable or $\mathbf{G}(\mathbf{P}_0 + k \cdot \vec{d}, \mathbf{V}_k^I, C_I) \leq 0$ is violated.

Definition 2: Voltage limit \mathbf{V}_{limit} is defined as the solution of $\mathbf{F}(\mathbf{P}_{cr}, \mathbf{V}_K^0, C_0) = 0$, i.e.

$$\mathbf{F}(\mathbf{P}_{cr}, \mathbf{V}_{limit}, C_0) = 0 \quad (5)$$

Suppose the function relationship between load \mathbf{P} and voltage \mathbf{V} under base case determined by $\mathbf{F}(\mathbf{P}, \mathbf{V}, C_0)$ is

$$\mathbf{V} = \mathbf{G}(\mathbf{P}) \quad (6)$$

We can also have

$$\mathbf{V}_{limit} = \mathbf{G}(\mathbf{P}_{cr}) \quad (7)$$

Assumption The function described by (6) is decreasing function, i.e., with the increasing of \mathbf{P} , \mathbf{V} is decreasing. By physical experiences it can be seen that this assumption is usually correct.

Under the above define 1, define 2, and assumption, it can be concluded that If real time operation voltage $\mathbf{V} > \mathbf{V}_{limit}$, for any contingency C_i ($i = 0, 1, 2, \dots, N$), the post-contingency operation point exists and this post-contingency operation point satisfies the inequality constraints; If real time operation voltage $\mathbf{V} < \mathbf{V}_{limit}$, there at least exists one

contingency C_i such that the post-contingency operation point doesn't exist or this post-contingency operation point violates the inequality constraints.

The above method for finding bus voltage limits can be generalized to find the limits of any other operating parameters which are *monotonous functions* of load vector \mathbf{P} . Suppose that r is some operating parameter such as line/corridor transfer power, the function relationship between r and load vector \mathbf{P} under base case is

$$r = h(\mathbf{P}) \quad (8)$$

Case I: $r = h(\mathbf{P})$ is decreasing function.

Under this situation, we get a lower limit r_{limit}^{lower} for r , which is corresponding to the critical power defined in (4)

$$r_{limit}^{lower} = h(\mathbf{P}_{cr}) \quad (9)$$

For operating under base case, $r > r_{limit}^{lower}$ is considered as secure and $r < r_{limit}^{lower}$ is considered insecure.

Case II: $r = h(\mathbf{P})$ is increasing function.

Under this situation, we get an upper limit r_{limit}^{upper} for r , which is also corresponding to the critical power defined in (4)

$$r_{limit}^{upper} = h(\mathbf{P}_{cr}) \quad (10)$$

For operating under base case, $r < r_{limit}^{upper}$ is considered as secure and $r > r_{limit}^{upper}$ is considered insecure.

In order to make the operate limits gotten more reliable, the voltage limit gotten by (7) or the limit of any other operate parameter gotten by (9) or (10) can be corrected as the following

$$\mathbf{V}_{limit} = \mathbf{G}(\mathbf{P}_{cr} - \Delta\mathbf{M}) \quad (11)$$

$$r_{limit}^{lower} = h(\mathbf{P}_{cr} - \Delta\mathbf{M}) \quad (12)$$

$$r_{limit}^{upper} = h(\mathbf{P}_{cr} - \Delta\mathbf{M}) \quad (13)$$

In (11), (12), (13), $\Delta\mathbf{M}$ represents imposed limit margin.

V. PROCEDURE FOR GETTING CRITICAL BUSES AND CRITICAL BRANCHES

Usually identification of critical buses and critical branches is done by modal analysis of load flow Jacobian matrix. To large power systems modal analysis is time consuming. Here we adopt a heuristic method to identify critical buses and critical branches. Voltage limits for critical buses and transfer power limits of critical branches will be visualized for monitoring.

A. Critical Buses

Fig. 6 shows PV-curves of different buses under the same contingency. The sensitivity $\Delta V_i / \Delta P$ of bus i is used as the index to identify the critical buses under this contingency. The larger $\Delta V_i / \Delta P$, the more critical bus i seems to be, e.g., bus

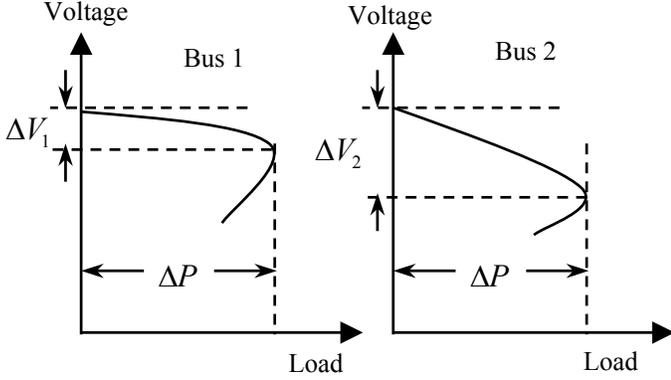


Fig. 6 PV-curves of different buses under the same contingency

2 is more critical than bus 1. We can choose M buses with the largest index $\Delta V_i / \Delta P$ as critical buses under this contingency. Critical buses for the whole system can be gotten by integrating critical buses under each contingency among the top N contingencies in severity (the number M , N can be set by users). That is

$$CB = \bigcup_{i=1}^N CB_i \quad (14)$$

where CB is the full critical bus set for the whole system, CB_i is the critical bus set under the i^{th} contingency.

B. Critical Branches

Procedure for getting critical branches under some contingency is similar to the above procedure for getting critical buses. The index to identify the critical interfaces is $|\Delta P_{i,b} / \Delta P|$. The larger $|\Delta P_{i,b} / \Delta P|$, the more critical branch i seems to be. We can choose M branches with the largest index $|\Delta P_{i,b} / \Delta P|$ as critical branches under this contingency.

Critical branches for the whole system can be gotten by integrating critical branches under each contingency among the top N contingencies in severity. That is

$$CI = \bigcup_{i=1}^N CI_i \quad (15)$$

where CI is the full critical branch set for the whole system, CI_i is the critical branch set under the i^{th} contingency.

VI. NUMERICAL EXAMPLE

The method proposed in this paper has been tested on several practical power systems. Here test results of a practical power system with 568 buses, 657 lines, 173 generators, 321 loads, 47 fixed shunts, 62 switched shunts/SVCs, 181 fixed transformers, 191 adjustable transformers are given out. In this test, load increase direction is that all loads in the whole system increase uniformly. We choose the top 10 contingencies in severity as critical contingencies, and in each

contingency we find 20 critical buses and 20 critical branches. Critical buses and branches of each contingency among the top 10 severe contingencies are integrated together to form the full critical bus set and critical branch set of the whole system. Note that it's possible for one bus or branch to be critical bus or critical branch in more than one contingency. The test results include the following parts. Limited to space, here the details of the following parts are not given out.

A. Top 10 Severe Contingencies

Information about top 10 contingencies with minimal the P-load limits are listed out, including contingency name, P-load limit value, fault information, critical buses, critical branches and some user-defined corridors. The number of selected severe contingencies can be set by users.

B. Critical Buses

After integrating 20 critical buses of each contingency among the top 10 contingency in severity, the full critical bus set is acquired. Voltage limits for these critical buses are given out. The number of selected critical buses under each contingency can be set by users.

C. Special Monitoring Buses

Special monitoring buses are set by users. Voltage limits of these buses will be listed out whether they are critical buses or not.

D. Critical Branches

After integrating critical branches of each contingency among the top 10 contingencies in severity, the full critical branch set is acquired. Branch transfer power limits are given out. The number of selected critical branches under each contingency can be set by users.

E. Special Monitoring Branches

Special monitoring branches are set by users. Branch transfer power limits of these branches will be listed out whether they are critical branches or not.

F. Corridors

Corridor transfer power limits are listed out. The number of corridors and branches each corridor includes can be set by users.

VII. CONCLUSION

This paper gives out a method for determining power system voltage security operation limits in a practical voltage security analysis (VSA) system based on VSAT software for large power systems. These operation limits include bus voltage limits, branch/corridor transfer power limits and P-load limit of the whole system.

The voltage security operation limits are determined by the most critical contingency among the studied contingency set. The most critical contingency determines the P-load limit of the whole system, and all kinds of operation parameter limits are operation parameter values corresponding to this P-load limit under pre-contingency. An operation parameter limit is upper limit if the function relationship between this operation

parameter and load power is an increasing curve, or lower limit if the function relationship between this operation parameter and load power is an decreasing curve. These operation parameter limits are helpful for operators to monitor the system operation state. The proposed method has been tested on several practical and it seems to work well.

In the future, voltage security control module should be included in this VSA system. That is, when power system voltage security margin is found not enough, proper control actions should be given out by VSA system. Mathematically, how to determine proper control actions comes down to a nonlinear optimization problem. Sensitivities of voltage security margin to various control parameters should be key information to determine proper control actions [3, 4].

VIII. REFERENCES

- [1] C. W. Taylor, *Power System Voltage Stability*, McGraw Hill, 1994
- [2] Y. Ou, and C. Singh, "Assessment of available transfer capacity and margins," *IEEE Trans. Power System.*, Vol. 17, pp. 463-468, May 2002.
- [3] R. A. Schlueter, S. Z. Liu, and K. B. Kilani, "Justification of the voltage stability security assessment and diagnostic procedure using a bifurcation subsystem method," *IEEE Trans. Power System*, vol. 15, pp. 1105-1111, Aug. 2000.
- [4] S. Greene, I. Dobson, and F. L. Alvarado, "Sensitivity of transfer capability margins with a fast formula," *IEEE Trans. Power System.*, Vol. 17, pp. 34-40, Feb. 2002.
- [5] *VSAT user manual*, PowerTech Lab. Inc., Canada, 2003

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