

# Application of Unified Power Flow Controller in Interconnected Power Systems—Modeling, Interface, Control Strategy, and Case Study

Zhengyu Huang, Yixin Ni, C. M. Shen, Felix F. Wu, Shousun Chen, and Baolin Zhang

**Abstract**—In this paper, a new power frequency model for unified power flow controller (UPFC) is suggested with its dc link capacitor dynamics included. Four principal control strategies for UPFC series element main control and their impacts on system stability are discussed. The main control of UPFC series element can be realized as a combination of the four control functions. The supplementary control of UPFC is added for damping power oscillation. The integrated UPFC model has then been incorporated into the conventional transient and small signal stability programs with a novel UPFC-network interface. Computer tests on a 4-generator interconnected power system show that the suggested UPFC power frequency model and the UPFC-network interface method work very well. The results also show that the suggested UPFC control strategy can realize power flow control fairly well and improve system dynamic performance significantly.

## I. INTRODUCTION

IN RECENT years, new types of FACTS devices have been investigated that may be used to increase power system operation flexibility and controllability, to enhance system stability and to achieve better utilization of existing power systems. The unified power flow controller (UPFC) [1]–[6] is a member of the FACTS family with very attractive features. The development of the first commercial UPFC is being carried out under joint sponsorship of the EPR1, AEP and Westinghouse of USA [7]. Analogue and digital simulators are developed in order to get deeper insight of UPFC mechanism, control strategy and dynamic performance. In digital simulation, electromagnetic transient programs are widely used for UPFC analysis [8]. However in the study of power system transient and steady-state stability, a power frequency model for UPFC is required to interface UPFC to the quasisteady-state model of ac transmission network and to analyze its effects on large-scale power systems. Some papers [3], [4] suggest UPFC power frequency models which were limited to be used in one machine to infinite bus system. Some other papers [5], [6] developed UPFC power frequency models with its dc link capacitor dynamics neglected or with some other approximations that might lead to undesirable error.

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In this paper, a new power frequency model for UPFC is suggested with its dc link capacitor dynamics included and is suitable to be used in the study of UPFC effects on the real power system dynamic behavior. A novel UPFC-network interface is also developed. In this model the UPFC shunt element control is to keep the UPFC terminal ac bus voltage and the dc link capacitor voltage constant. Four principal control strategies for UPFC series element main control, i. e. line power flow control, series compensation control, transformer-like control, and phase-shifter-like control, and their impacts on system stability are discussed. The final UPFC series element control used can be a combination of the four control outputs through changeable weighting factors. Thus to make full use of UPFC in system control and stability is equivalent to optimize the weights of each control function. The supplementary control of UPFC is added to damp power oscillation along the tie lines after disturbances. The integrated UPFC model has been incorporated into the conventional power system transient and small signal stability programs with the novel UPFC-network interface. Its application is illustrated with a 4-generator interconnected power system [9]. The computer results show that the suggested UPFC power frequency model and the UPFC-network interface method work very well in the study of system dynamic behavior. The results also show that the suggested UPFC control strategy can realize constant power flow control fairly well and can improve system dynamic performance significantly.

## II. MATHEMATICAL MODEL

### A. UPFC Mathematical Model

Fig. 1 shows a schematic diagram for UPFC, where  $n_1$ ,  $X_{t1}$  and  $n_2$ ,  $X_{t2}$  are the voltage ratio and the reactance of the shunt and series transformers respectively. All the variables used in UPFC model are denoted in Fig. 1 with bold fonts representing phasors.

Per unit system and MKS units are jointly used in modeling. The ac system uses per unit system with its variables calculated based on the system-side  $S_B$  and  $V_B$ , while the dc variables are expressed in MKS units. We first consider the UPFC dc link capacitor charging dynamics. The dc currents  $I_{d1}$ ,  $I_{d2}$  (see Fig. 1) and the capacitor voltage and current have the following relation with harmonics neglected:

$$\begin{cases} I_d = C \frac{dV_d}{dt} \\ I_d = I_{d1} + I_{d2} \end{cases} \quad (1)$$

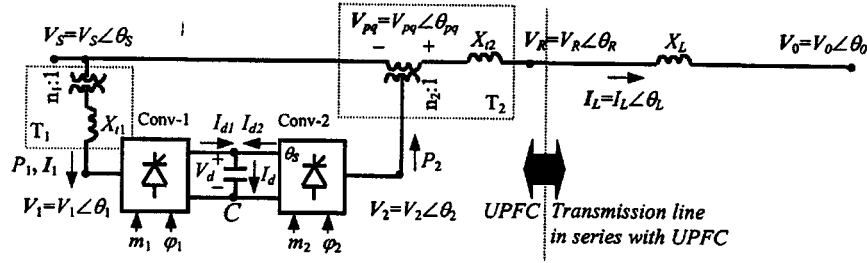


Fig. 1. Transmission line with UPFC installed.

If we assume the inverters are ideal, the real power exchange with the ac system will be ( $P_1$  and  $P_2$  are in p. u.):

$$\begin{cases} P_1 = V_d I_{d1} / S_B \\ P_2 = -V_d I_{d2} / S_B \end{cases} \quad (2)$$

From equations (1) and (2), we have:

$$CC_d = \frac{dV_d}{dt} (P_1 - P_2) S_B \quad (3)$$

From ac system, we know that  $P_1$  and  $P_2$  can be calculated by (see Fig. 1):

$$\begin{cases} P_1 = \text{Re}(\dot{V}_1 \dot{I}_1^*) = \text{Re}(\dot{V}_1 (\frac{n_1 \dot{V}_S - \dot{V}_1}{jX_{t1}})^*) \\ P_2 = \text{Re}(\dot{V}_{pq} \dot{I}_L^*) = \text{Re}(\dot{V}_{pq} (\frac{\dot{V}_S + \dot{V}_{pq} - \dot{V}_R}{jX_{t2}})^*) \end{cases} \quad (4)$$

Applying modern PWM control technique[10] to the two voltage source converters, the relations between the inverter dc- and ac-side voltages can be expressed by:

$$\begin{cases} V_1 = m_1 V_d / V_B \\ V_2 = m_2 V_d / V_B \end{cases} \quad (5a)$$

where coefficients  $m_1$  and  $m_2$  represent the PWM control effects in order to maintain desired inverter ac-side voltages  $V_1$  and  $V_2$  respectively. The desired  $m_1$  and  $m_2$  are UPFC main control outputs (see Section II-B).  $V_1$  and  $V_2$  are in p. u. and  $V_B$  is the ac system base voltage.

The phase angles of  $\dot{V}_1$  and  $\dot{V}_2$  are denoted as  $\theta_1$  and  $\theta_2$  respectively. They are controlled through firing angle  $\varphi_1$  and  $\varphi_2$  of the two converters and have the relation w. r. t. the phase angle of  $\dot{V}_S$  as follows:

$$\begin{cases} \theta_1 = \theta_S - \varphi_1 \\ \theta_2 = \theta_S - \varphi_2 \end{cases} \quad (5b)$$

The desired  $\varphi_1$  and  $\varphi_2$  are UPFC main control outputs as well (see Section II-B).

Finally, taking the series transformer ratio into consideration, and rewriting equations (1)~(5), the UPFC power frequency model used in dynamic study will be:

$$CV_d \frac{dV_d}{dt} = (P_1 - P_2) S_B$$

$$\text{where } P_1 = \text{Re}(\dot{V}_1 (\frac{n_1 \dot{V}_S - \dot{V}_1}{jX_{t1}})^*),$$

$$P_2 = \text{Re}(\dot{V}_{pq} (\frac{\dot{V}_S + \dot{V}_{pq} - \dot{V}_R}{jX_{t2}})^*)$$

$$V_1 = m_1 V_d / V_B$$

$$\theta_1 = \theta_S - \varphi_1$$

$$V_{pq} = m_2 V_d / V_B / n_2$$

$$\theta_{pq} = \theta_S - \varphi_2 \quad (6)$$

Since there are two network interface (complex) equations, and the desired  $m_1$ ,  $\varphi_1$ ,  $m_2$  and  $\varphi_2$  can be obtained from UPFC main control system, therefore based on equation (6) together with UPFC control system equations and ac network interface equations we can analyze UPFC dynamics without difficulty. The first three equations are for state variable  $V_d$  calculation and the latter 4 equations are for network interface calculation which will be discussed in detail later. It is clear that the UPFC power frequency model derived above can fit various control strategies and multi-machine power system stability analysis.

### B. UPFC Main Control

The UPFC main control includes both shunt and series elements control. A simplified control system block diagram for UPFC is shown in Fig. 2. The shunt element control is shown in Figs. 2(a) and (b): the constant dc link capacitor voltage control realized by controlling the firing angle  $\varphi_1$  of inverter 1 and the constant UPFC terminal bus voltage control achieved by controlling  $m_1$  of the PWM controller of inverter 1. PI controller should be used if no control error is allowed. Figs. 2(c) to (f) are the four principal control schemes suggested in [1] for the series element main control. They are constant real and reactive power flow control (Fig. 2(c)), constant series compensation control (Fig. 2(d)), transformer-like control (Fig. 2(e)) and phase-shifter-like control (Fig. 2(f)). The UPFC output series compensation voltage  $\dot{V}_{pq} = V_{pq} \angle \theta_{pq}$  ( $\theta_{pq} = \theta_2 = \theta_S - \varphi_2$ ) can be decomposed as  $V_p$  and  $V_q$  (see Fig. 2(h)). The former is perpendicular to  $\dot{V}_S$  and has strong impacts on real power flow; and the latter is in phase with  $\dot{V}_S$  and has significant effects on reactive power flow as we all know. From Figs. 2(c) to (f), the desired  $V_p$  and  $V_q$  pair corresponding to a specific principal control scheme can be obtained. Then the four pairs of  $V_p$  and  $V_q$  will be combined into a final pair of  $V_p$  and  $V_q$  through weighting factors (see Fig. 2(g)). After passing through a smooth block the final pair of  $V_p$  and  $V_q$  is converted into desired  $m_2$  and  $\varphi_2$  for PWM control and firing angle control of inverter 2. It is clear that by setting proper weighting  $w_i$ , the preferred control type(s) will be formed. It can be expected that using optimization theory to generate time varying weighting

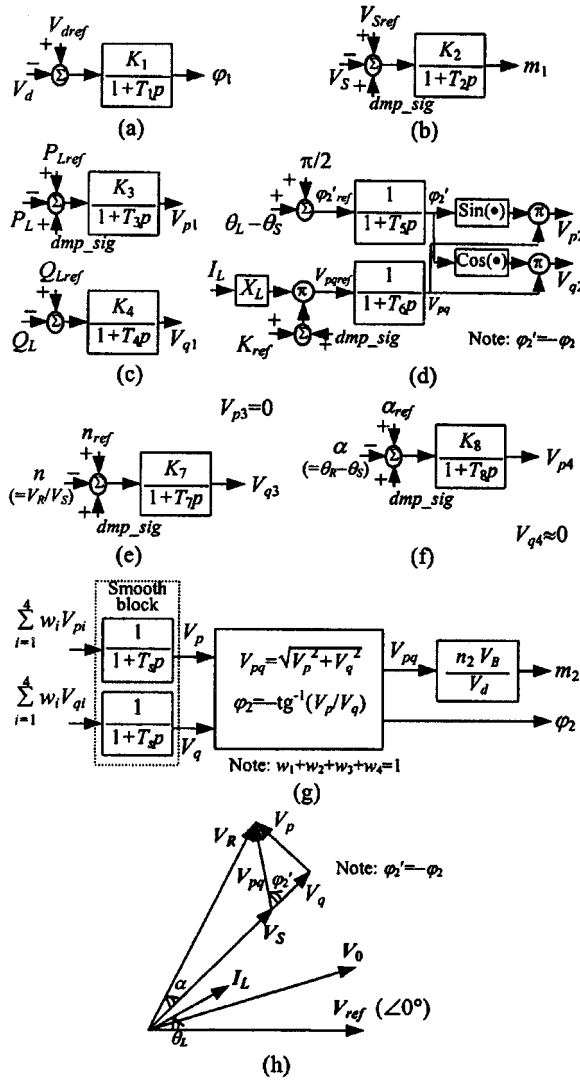


Fig. 2. The main control and phasor diagram.

factor  $w_i$ , the UPFC could achieve its best steady-state and dynamic performance. At this stage only crisp  $w_i$  (1 or 0) and the simplified first order model is used for the four principal controls. In fact there is no difficulty to include detailed control models.

In Fig. 2(d),  $\phi'_{2ref} = \pi/2 + \theta_L - \theta_S$  and  $V_{pqref} = K_{ref} I_L X_L$  are calculated according to the real time measurements of  $(\theta_L - \theta_S)$  and  $I_L$ .  $K_{ref}$  is the desired degree of series compensation of the tie line. In phase-shifter-like control, an approximation is made, i.e. assuming the desired  $V_q$  is zero. The modulation signal  $dmp\_sg$  in Figs. 2(c) to 2(f) is for damping the power oscillation of the interconnected power system which is the output of UPFC supplementary control and will be discussed in Section III below. In Fig. 2(g), the block of  $\frac{n_2 V_B}{V_d}$  is derived from equation (6). Fig. 2 and equation (6) constitute the integrated UPFC power frequency model for power system stability analysis. It is clear that there is no limitation to the control system transfer function used. Therefore the concept of user-defined-control [11] can be used for UPFC in order to study the effects of control transfer functions on the integrated power system dynamics.

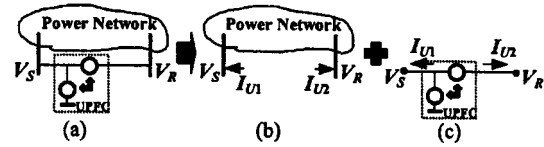


Fig. 3. The interface of the UPFC to the network.

### C. Mathematical Models for Other Power System Elements

The mathematical models for other power system elements are as follows. The subtransient model is used for the generators with simplified third-order excitation control [12]. The mechanical power of each generator is taken as a constant. Loads are expressed as constant impedance and the ac network is linear. In order to simplify the software development, we assume  $X''_d = X''_q = X''$  for generators so that  $X''$  can be included in the bus admittance matrix, and the latter can be reduced to generator internal buses [12] and used for ac network interface to UPFC which will be discussed in detail below.

### D. Interface of UPFC to the AC Network

The interface calculation of UPFC to ac network will have significant impacts on transient stability analysis speed and accuracy. The sequential solution method, which has been successfully used for ac-dc interface in ac/dc power system load flow and transient stability analysis, has been extended here for UPFC interface to ac network (see Fig. 3).

In the interface calculation we assume that the bus admittance matrix has been reduced to generator internal buses with UPFC ac terminal buses remained. The corresponding reduced bus admittance matrix equation takes the form:

$$\begin{bmatrix} Y_{GG} & Y_{GU} \\ Y_{UG} & Y_{UU} \end{bmatrix} \begin{bmatrix} \dot{E}_G \\ \dot{V}_U \end{bmatrix} = \begin{bmatrix} \dot{I}_G \\ \dot{I}_U \end{bmatrix} \quad (7)$$

where  $\dot{E}_G$ : generator internal voltage behind the subtransient reactance  $X''$ .  $\dot{V}_U = [\dot{V}_S, \dot{V}_R]$ : ac terminal bus voltages of the UPFC (see Fig. 3).  $\dot{I}_G$ : generator currents injecting to the network.  $\dot{I}_U = [\dot{I}_{U1}, \dot{I}_{U2}]$ : UPFC currents injecting to the network (see Fig. 3).

The UPFC currents injecting to the ac network can be expressed by (see Figs. 1 and 3):

$$\begin{cases} \dot{I}_{U1} = -\frac{n_1 \dot{V}_S - \dot{V}_1}{jX_{t1}} n_1 - \frac{\dot{V}_S + \dot{V}_{pq} - \dot{V}_R}{jX_{t2}} \\ \dot{I}_{U2} = \frac{\dot{V}_S + \dot{V}_{pq} - \dot{V}_R}{jX_{t2}} \end{cases} \quad (8)$$

Suppose all the state variables at  $t = t_{n+1}$  have been predicted according to the numerical integration and now we are going to make network solution for algebraic variables. It is clear that in equation (8), though the UPFC output voltage magnitudes  $V_1$  and  $V_{pq}$  can be known from control output and equation (6), the phase angles of  $\dot{V}_1$  and  $\dot{V}_{pq}$  are unknown since they depend on the phase angle of  $\dot{V}_S$ . The phase angle of  $\dot{V}_S$  is unknown that should be obtained from the network solution. Therefore an iteration approach is required to obtain the network solution.

The iteration is conducted as follows. Substituting equation (8) into equation (7), and rearranging the second equation of equation (7), we finally have:

$$\begin{cases} \dot{I}_{1G} + \dot{I}_{2G} + (Y_{SS} + Y_{RS} + \frac{n_1^2}{jX_{t1}})\dot{V}_S + (Y_{SR} + Y_{RR})\dot{V}_R \\ = \frac{n_1}{jX_{t1}}\dot{V}_1 \\ \dot{I}_{2G} + (Y_{RS} - \frac{1}{jX_{t2}})\dot{V}_S + (Y_{RR} + \frac{1}{jX_{t2}})\dot{V}_R = \frac{1}{jX_{t2}}\dot{V}_{PQ} \end{cases} \quad (9)$$

where  $\begin{bmatrix} Y_{SS} & Y_{SR} \\ Y_{RS} & Y_{RR} \end{bmatrix} = Y_{UU}$ , and  $\begin{bmatrix} \dot{I}_{1G} \\ \dot{I}_{2G} \end{bmatrix} = Y_{UG}\dot{E}_G$  which can be calculated according to generator state variables. If we define a constant matrix:

$$\tilde{Y}_{UU} = \begin{bmatrix} (Y_{SS} + Y_{RS} + \frac{n_1^2}{jX_{t1}}) & (Y_{SR} + Y_{RR}) \\ (Y_{RS} - \frac{1}{jX_{t2}}) & (Y_{RR} + \frac{1}{jX_{t2}}) \end{bmatrix} \quad (10)$$

and a fictitious time varying current vector:

$$\tilde{I}_U = \begin{bmatrix} \frac{n_1}{jX_{t1}}\dot{V}_1 - (\dot{I}_{1G} + \dot{I}_{2G}) \\ \frac{1}{jX_{t2}}\dot{V}_{PQ} - \dot{I}_{2G} \end{bmatrix} \quad (11)$$

we have:

$$\tilde{Y}_{UU}\dot{V}_U = \tilde{I}_U \quad (12)$$

The equations (10) to (12) are used for the iteration of UPFC-network interface as follows:

- Step 1: Estimate the initial voltages of  $\dot{V}_S^{(0)}$  and  $\dot{V}_R^{(0)}$  (e. g. according to the last step) and calculate  $\tilde{I}_U$  based on equations (11) and (6).
- Step 2: Solve equation (12) for  $\dot{V}_U = (\dot{V}_S^{(1)}, \dot{V}_R^{(1)})'$ . If the difference of  $(\dot{V}_S^{(1)}, \dot{V}_R^{(1)})$  and  $(\dot{V}_S^{(0)}, \dot{V}_R^{(0)})$  is less than the given tolerance then  $\dot{V}_S^{(1)}$  and  $\dot{V}_R^{(1)}$  are considered as the solution of equation (9). Otherwise go to step 3
- Step 3: Update  $(\dot{V}_S^{(0)}, \dot{V}_R^{(0)})$  by  $(\dot{V}_S^{(1)}, \dot{V}_R^{(1)})$  and repeat steps 1 and 2 till convergence is reached.

It is clear that Step 1 corresponds to the UPFC calculation with estimated terminal voltages (see Fig. 3(c)). Step 2 corresponds to ac network solution with UPFC effect represented as an injecting current to ac network (see Fig. 3(b)) except that we defined a fictitious current vector as the UPFC-network interface variable to save CPU time. After the solution of interface voltages  $\dot{V}_U = (\dot{V}_S, \dot{V}_R)'$  the first part of matrix equation (7) is used for calculation of generator injecting current  $\dot{I}_G$ .

By this iteration approach there is no need to rewrite the entire program. The stability analysis can be realized simply by inserting just a few new subroutines into the existing software. The network solution accuracy can be controlled via the interface tolerance setting. This interface can also be extended and used for other FACTS devices.

### III. DISCUSSION OF UPFC SERIES ELEMENT MAIN CONTROL SCHEME

As suggested in [1] there are four principle control schemes for the series compensation voltage  $\dot{V}_{PQ}$  i. e. constant real and reactive power flow control constant series compensation control phase-shifter-like control and transformer-like control. At the normal operation the constant real and reactive power control is

the most attractive especially for the tie line power flow control. However under a large disturbance such as a three phase fault this control trying to keep constant power flow over the tie line might not be appropriate for system transient stability. What is the best control strategy for the first swing stability is our current concern.

In a one-machine-to-infinite-bus system according to equal area criterion it is well known that the best control in the first swing is the one which can lead to the largest electric power transfer from generator to receiving end and hence the largest rotor deceleration power.

For the constant real and reactive power control an efficient way to improve first swing stability is fast increasing power reference for a certain period which can lead to fast power transfer so as to release rotor kinetic energy. However it is difficult to know the optimal reference power and optimal time duration because the fault severity is unforeseeable. The phase-shifter-like control will not change the shape of the power-angle curve if we consider the phase-shifter-like control is equivalent to a constant generator power angle shift during the first swing, and the total available deceleration area has no significant change. So this control is not so efficient in improving system transient stability. The transformer-like control is basically a voltage control which may increase the peak of the power-angle curve by 10-20% in order to avoid overvoltage. The total deceleration area increase is still not so good. For the constant series compensation control, if compensation degree  $K_{ref}$ , is high enough (here we assume the inverter has enough capacity), the power-angle characteristics can rise significantly via reducing tie line apparent reactance which is clearly beneficial to the system transient stability.

Based on the discussion above we can see that during the first swing, there are mainly two approaches to improve first swing stability. One is fast increasing of power reference for a certain period, and the other is to switch the series element control from constant power control to constant series compensation control. However when the rotor reaches the maximum angle in the first swing, the constant series compensation control should be switched to some other control, otherwise this control will over-decelerate the rotor and lead to undesirable large power oscillation in the subsequent swings.

In the subsequent swings, supplementary control will play an important role in damping the power oscillation on the tie lines. The supplementary control can be applied to the shunt element control via voltage modulation or to the series element via power modulation, voltage modulation, phase-shift-angle modulation or series compensation degree modulation depending on the control strategy currently used. In order to avoid unnecessary switching of control strategy, right after the first swing the control returns to the constant real and reactive power control. Therefore two different damping schemes are tested in this paper. One is the voltage modulation in voltage control of the shunt element (see *dmp\_sig* in Fig. 2(b)). The other is power modulation in constant power control of the series element (see *dmp\_sig* in Fig. 2(c)). In our paper the input signal used for supplementary control is the tie line real power flow or its terminal voltage phase angle difference. The supplementary control has a PSS-like transfer function (see Fig. 4) with its output signal connected to the main control shown in Fig. 2(b) or (c).

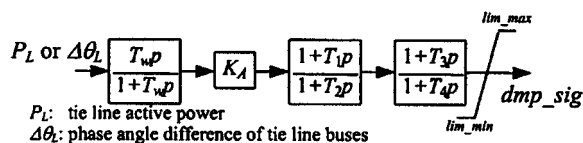


Fig. 4. Supplementary control block diagram.

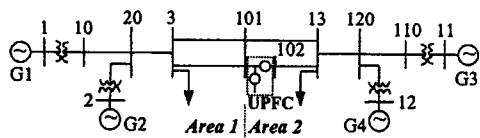


Fig. 5. The computer test system diagram.

As a summary the control scheme used in our paper is as follows: the constant real and reactive power control is used for the series element control at the steady state. It will be switched to constant series compensation control with higher compensation degree during the first swing. The switching can be triggered by large disturbance signal (such as  $\Delta R_L/\Delta t$ ). The series element control will return to the constant power control right after the peak point of the first swing. The shunt element control is constant UPFC ac bus voltage control together with constant dc link capacitor voltage control. The supplementary control is added on the shunt element via voltage modulation or added on the series element via real power modulation. The control system output variables are  $m_1$ ,  $m_2$ ,  $\varphi_1$  and  $\varphi_2$ .

#### IV. COMPUTER TEST RESULTS

##### A. Test System and Studied Cases

A 4-generator 2-area interconnected power system [9] is used for the computer test (see Fig. 5). At the steady state, the electric power output of generators 1 to 4 is 700 MW, 700 MW, 716 MW, and 700 MW respectively. The loads on the buses 3 and 13 are 967 MW and 1767 MW. About 390 MW power is transferred from area 1 to area 2 with a UPFC installed on one of the tie lines. The generator, exciter and load models are the same as described in Section II-C. The disturbance used is a three-phase stub fault on bus 3 at  $t = 0.50$  second, and cleared in 0.10 second. Three groups of cases are tested, i.e., group 1: system first-swing transients without supplementary control of UPFC; group 2: supplementary control (S. C.) effects on system dynamic behavior; and group 3: suggested control strategy and its effect on system dynamic behavior.

All the studied cases are listed in Table I with corresponding case ID and conditions. The control parameters are presented in the appendix. The computer test results of each group are described below.

##### B. Computer Results of the First Swing Transients

The computer test results for case 1a (without UPFC) are shown in Fig. 6, where Fig. 6(a) is the rotor angle plot and Fig. 6(b) is real power on line 102-13. Figs. 7 and 8 are for cases 1b (UPFC with constant tie line  $P$  and  $Q$  control) and 1c (UPFC with constant series compensation control) respectively.

TABLE I  
TEST CASE ID AND THE CORRESPONDING CONDITIONS

Group #	Case ID	UPFC	Main Ctrl.	Supp. Ctrl.	
				Inp. Sig.	Mod. Elem.
1	1a	N	---	---	---
	1b	Y	Const. P/Q	---	---
	1c	Y	Const. C. ( $K_{ref}=0.4$ )	---	---
2	2a	Y	Const. P/Q	$P_L$	shunt
	2b	Y	Const. P/Q	line $\theta$ diff.	shunt
	2c	Y	Const. P/Q	$P_L$	series
	2d	Y	Const. P/Q	line $\theta$ diff.	series
3	3	Y	*	$P_L$	shunt

\* see suggested control strategy at the end of Section 3

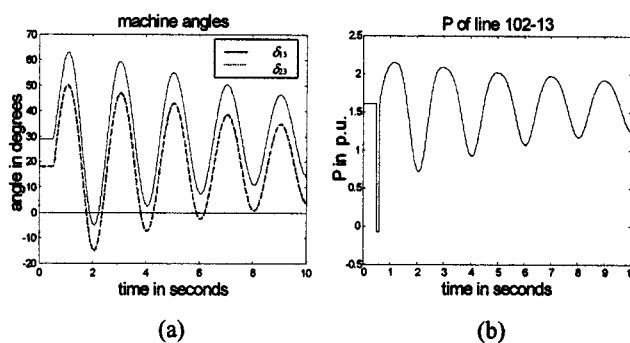


Fig. 6. Plots of case 1a.

The figures include (a) rotor angles, (b) voltages of the fault bus 3 and UPFC sending-end bus 101, (c) UPFC-installed tie line real power and (d) dc capacitor voltage  $V_d$ . The following observations can be made:

- When UPFC has constant power flow control, the first swing peak angle (Fig. 7(a)) is almost the same as and even a little bit worse than case 1a (Fig. 6(a)) in which there is no UPFC in the system. This means the constant power flow control might have negative effect on system first swing transient if there is no fast reference power increase during the period.
- When UPFC has constant series compensation control, the first swing peak angle reduced noticeably (Fig. 8(a)) which is consistent to our discussion. We can also see that the line power increases greatly in the first swing (Fig. 8(c)) to fast release the rotor kinetic energy.
- After first swing, the constant power flow control can recover the controlled tie line power to the setting point very quickly (Fig. 7(c)) which is extremely desirable. However the constant series compensation control case will have long period of power oscillation on the controlled tie line (Fig. 8(c)).
- The time simulation shows very good convergence in UPFC-network interface which proves that the suggested UPFC model and interface method work very well in transient stability analysis.
- The dc capacitor voltage fluctuation during transient (Figs. 7(d) and 8(d)) is less than 0.3kV, about 1.5% of its

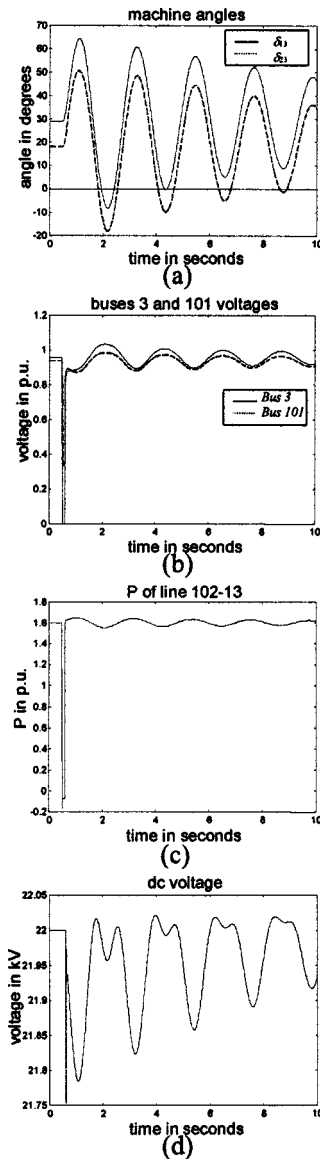


Fig. 7. Plots of case 1b.

rated value 22kV, which means to neglect dc link capacitor dynamics may cause some error but it is acceptable to certain extent.

- The rotor angle plots in Figs. 7 and 8 also show that system damping is very poor and supplementary damping control is necessary.

### C. Effects of UPFC Supplementary Control

Four types of supplementary control are tested. They are cases 2a to 2d (see Table I) and all with constant power flow control for the series element. The differences are the input signal of supplementary control and the modulation mode (see Table I). Only curves for cases 2a and 2c are presented in Fig. 9. The computer results show:

- The parameters of supplementary control for cases 2a to 2d are properly designed to get desired damping effect for the interarea mode. Therefore for all the four cases, the system damping is greatly improved (see Figs. 9(a) for

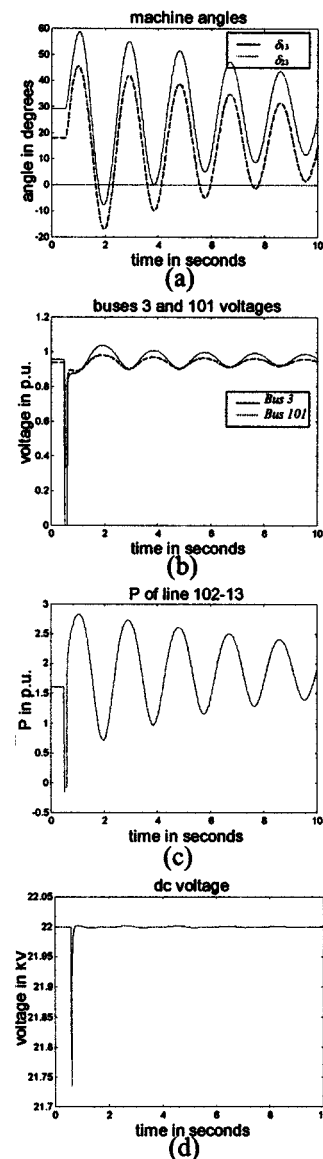


Fig. 8. Plots of case 1c.

case 2a and Fig. 9(c) for case 2c as two examples) which means the input signal, transfer function, parameters of supplementary control are well selected and the modulation mode is effective.

- For all the four cases the input signal for supplementary control ( $\Delta P_L$  or  $\theta_L$ ) has similar 'shape' in time domain since  $\Delta P_L \approx \frac{V_{L1}V_{L2}}{X_L} \sin\theta_L \approx K\theta_L$  (if  $\theta_L$  is not very large), where  $V_{L1}$  and  $V_{L2}$  are sending and receiving end voltages of the tie line. Besides the transfer functions used are similar. Therefore the output of supplementary control signal is quite similar to each other (see Figs. 9(b) for case 2a and Fig. 9(d) for case 2c as two examples). The supplementary control output limits used are 0.2 p. u. for voltage modulation for avoiding over-voltage and 0.5 p. u. for power modulation for avoiding overload.
- The interarea mode for each case is as follows:

$$\text{w/o S. C.:} \quad -0.0808 + j2.8896$$

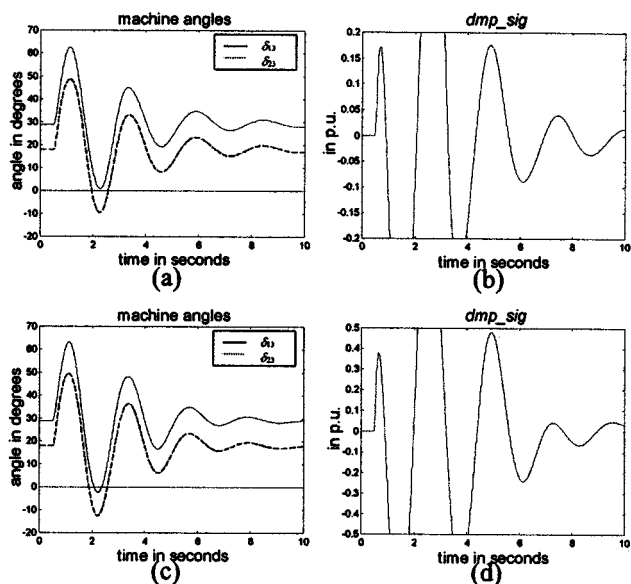


Fig. 9. Effects of supplementary control.

- Case 2a (shunt- $P_L$ ):  $-0.4306 + j2.4779$
- Case 2b (shunt- $\theta$ ):  $-0.5340 + j2.6586$
- Case 2c (series- $P_L$ ):  $-0.6174 + j2.7401$
- Case 2d (series- $\theta$ ):  $-0.6214 + j2.8915$

From eigenvalue analysis we can see the interarea damping has been improved significantly after supplementary control is added.

#### D. Computer Results of Suggested Control Strategy

From the above test and study, it can be seen that the performance of the UPFC is greatly affected by its control strategy. Computer results till now are consistent to our discussion in Section III. Now we test the suggested UPFC control scheme, i. e. constant power flow control at the steady state and after the first swing, constant series compensation control during first swing. The supplementary control is voltage modulation on shunt element control with tie line real power as input signal. The corresponding computer results are shown in Fig. 10. The plots include: (a) rotor angle, (b) buses 3 and 101's voltages, (c) real power flow of the two tie lines, (d) dc link capacitor voltage, (e) exchange power of inverters 1 and 2 with ac system, and (f) voltages  $V_1$  and  $V_{pq}$ . From the plots, we can see that the first swing, the system damping and the power flow control after large disturbance are all quite satisfactory. We can also see that the  $V_d$  fluctuation is very small and the power exchange of inverters with ac system is at reasonable level. The  $V_1$  and  $V_{pq}$  are also in the acceptable range. All the facts show that the suggested control scheme works very well in the system transients and can control interarea power flow efficiently and improve system dynamic behavior significantly.

#### V. CONCLUSION

The suggested UPFC power frequency model and the developed UPFC-network interface method work very well

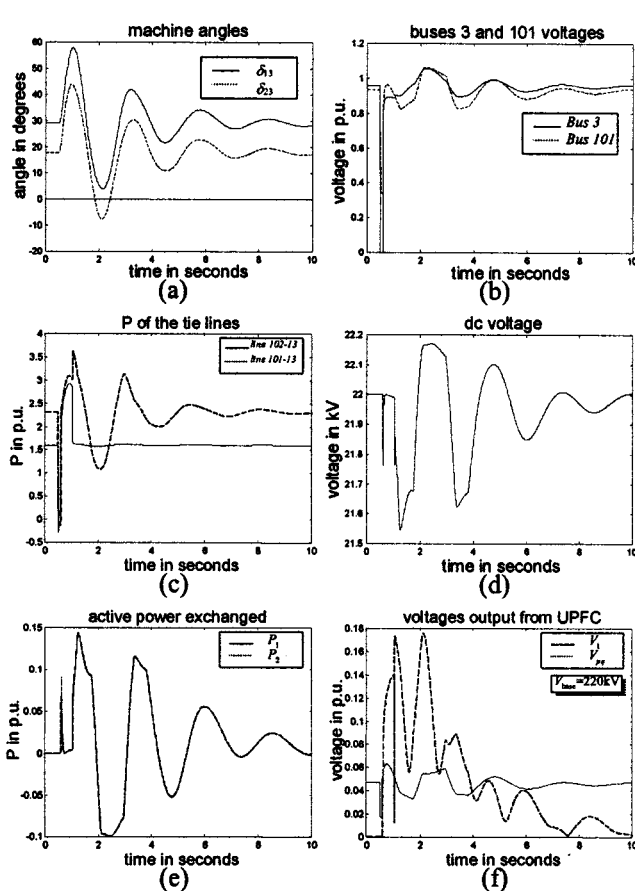


Fig. 10. Results of the suggested control scheme.

in the study of power system dynamics with satisfied convergence and accuracy. Four principal main control strategies are discussed and the computer tests results support the discussion conclusion very well. The constant power flow control is good for steady state control and the constant series compensation control is useful for first swing stability. The supplementary control is very efficient in damping intrarea power oscillation. The suggested UPFC control can realize the desired control strategy flexibly and improve system dynamic performance significantly.

#### APPENDIX

The parameters of UPFC:

$$V_B = 220 \text{ kV}, S_B = 100 \text{ MVA}, n_1 = 0.05, n_2 = 0.25, X_{t1} = 0.00025, X_{t2} = 0.05 \text{ (all in p. u. of 220kV base).}$$

The parameters of UPFC main control:

$$K_1 = 0.05, T_1 = 0.01, K_2 = 1.0, T_2 = 0.05, K_3 = 5.0, T_3 = 0.1, K_4 = 5.0, T_4 = 0.1, T_5 = 0.02, T_6 = 0.02, K_{ref} = 0.4, V_{dref} \approx 22 \text{ kV.}$$

TABLE II  
THE SUPPLEMENTARY CONTROL PARAMETERS (T IN Sec.)

Case ID	$K_A$	$T_w$	$T_1/T_3$	$T_2/T_4$	$lim\_max$	$lim\_min$
2a	0.83	3.0	0.02	0.33	0.2	-0.2
2b	2.83	3.0	0.02	0.23	0.2	-0.2
2c	0.83	3.0	0.02	0.19	0.5	-0.5
2d	7.17	3.0	0.02	0.23	0.5	-0.5

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