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<td>Wu, FF</td>
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Power Transfer Allocation for Open Access Using Graph Theory—Fundamentals and Applications in Systems Without Loopflow

Felix F. Wu, Fellow, IEEE, Yixin Ni, Senior Member, IEEE, and Ping Wei, Student Member, IEEE

Abstract—In this paper, graph theory is used to calculate the contributions of individual generators and loads to line flows and the real power transfer between individual generators and loads that are significant to transmission open access. Related lemmas are proved which present necessary conditions required by the method. Based on ac load flow solution a novel method is suggested which can decide downstream and upstream power flow tracing paths very fast and calculate the contribution factors of generations and loads to the line flows efficiently. The power transfer between generators and loads can also be determined. The suggested method is suitable for both active and reactive power tracings of real power systems.

Index Terms—Graph theory application, power flow tracing, transmission open access.

I. INTRODUCTION

POWER system deregulation and transmission open access make it more and more important to calculate the contributions of individual generators and loads to line flows and the real power transfers between individual generators and loads. The results can help to allocate the total cost of transmission among all the users in an equitable way.

Methods based on dc load flow and sensitivity analysis can’t consider accurately the reactive power transfer allocation and system nonlinearity. In [1], [2] a novel method is suggested to solve the problem however a matrix inverse calculation is required which is time consuming especially for a large scale power system. In [3] another approach is presented which introduces some new concepts such as domain, common, link and state graph and is suitable for large-scale power system applications. However there are no clear declaration and proof of the conditions required for the method.

In this paper graph theory [4], [5] is suggested to solve the problem. Graph theory is quite mature and especially suitable to tackle such network topology related issue. In our case directed graph is used. The vertices of the graph are system buses and the edges of the graph are lines and transformers. The direction of each edge is the direction of power flow inside. The directed graph of active power flow may be different from that of reactive power flow in edge directions.

Related lemmas are presented and proved first which presents the necessary conditions to guarantee the feasibility of the suggested method in power flow tracing. Then based on the widely used bus-line incident matrix, downstream and upstream tracing sequences are determined at ease. A novel approach is suggested to calculate the contribution factors of individual generations and loads to the line flows respectively. The real power transfer between individual generators and loads can also be determined. The method can be used for both active and reactive power transfer allocations. Based on ac load flow solution it can consider system nonlinearity accurately. The suggested method is very efficient and suitable for use in real power systems.

The paper is organized as follows. In Section II the basic assumptions are described. Related lemmas are presented and proved. Directed graphs for upstream and downstream tracings, the corresponding bus-inflow-line and bus-outflow-line incident matrices and their usage in power flow tracing are introduced. In Section III through upstream and downstream tracings, contribution factors of individual generators and loads to line flows are calculated respectively. The power transfers between generators and loads are determined as well. A simple example is used in Sections II and III to help the illustration of the method. Section IV presents the test results from the IEEE 30-bus system. Conclusions are made in Section V.

II. FUNDAMENTALS OF LOAD FLOW TRACING

In order to simplify the problem, we first make the following basic assumptions:

- An ac load flow solution is available from on-line state estimation or off-line system analysis. The studied system has finite number of buses. It is operated properly and there is no loop flow in the system.
- Real power and reactive power required by transmission line resistance, reactance and charging capacitance have been moved to the line terminal buses (see Section III for details) and modeled as “equivalent loads” according to ac load flow solution. Therefore the line active and reactive power flows keep constant along the line, each edge has a definite direction and the network is “lossless.”
- A generator has the priority to provide power to the load on the same bus. The remaining power will enter the network to supply other loads in the network to avoid unnecessary losses. It is true even according to a transaction contract a generator does not sell electricity to the local load. This is because electricity has no label and system operators have the authority to dispatch the power flow. Therefore the buses of a network can be classified as generator buses,
load buses and network buses based on their net injection to the system. This concept can be applied to both active and reactive power flows.

- The flows of electricity obey the proportional-sharing rule [1]–[3].

Based on the assumptions made above we can prove the following two lemmas:

**Lemma 1:** A lossless, finite-nodes power system without loop flow has at least one pure source, i.e., a generator bus with all incident lines carrying outflows.

This lemma will be proved below. It guarantees to start and continue a downstream tracing from an existing pure source.

**Lemma 2:** A lossless, finite-nodes power system without loop flow has at least one pure sink, i.e., a load bus with all incident lines carrying inflows.

This lemma can be proved similarly as lemma 1. It guarantees to start and continue an upstream tracing from an existing pure sink.

**Proof of Lemma 1:** We start from any bus with an incident line carrying inflows. Along this line we go upstream and come to a “next” bus. Here we assume in power system there is no self-loop [see Fig. 1(a)]. If there is no incident line carrying inflows to the next bus, then it is an existing pure source already. If there is at least one incident line carrying inflow to the next bus, then we can continue to go upstream along the line to another bus [see Fig. 1(b)]. This upstream tracing can continue if there is no pure source confronted. If the upstream bus is always a new bus, this will lead to the conclusion that the system has infinite buses. If the upstream bus is an old bus, that is a bus appeared in the previous tracing path, then a loop flow is existing in the system which conflicts with the assumption. Therefore a pure source does exist to end the upstream tracing.

**Proof of Lemma 2:** We start from any bus with an incident line carrying outflows. Along this line we go downstream and come to a “next” bus (still we assume there is no self-loop). If there is no incident line to carry outflows from the next bus, then it is an existing pure sink already. If there is at least one incident line carrying outflow from the next bus, then we can continue to go downstream along the line to another bus. This downstream tracing can continue if there is no pure sink confronted. If the downstream bus is always a new bus, this will lead to the conclusion that the system has infinite buses. If the downstream bus is an old bus, that is a bus appeared in the previous tracing path, then a loop flow is existing in the system which conflicts with the assumption. Therefore a pure sink does exist to end the downstream tracing.

Based on the two lemmas, we can use bus-line incident matrix to form bus-inflow-line and bus-outflow-line incident matrices respectively and determine the pure sink and pure source of the studied system quickly. This can be shown through an example [see Fig. 2(a)]. The system has 4 buses (buses 1 to 4) and 5 lines (lines a to e). The active power flow is used to form corresponding directed graph. The bus-line incident matrix (BLIM) is shown in Fig. 2(b).

BLIM can split into bus-outflow-line and bus-inflow-line incident matrices easily. The former is formed by all elements “1” in BLIM. The latter is formed by all elements “1” in BLIM and then all elements “−1” are changed to “1.” The resultant two matrices are shown in Fig. 2 where Fig. 2(c) is the bus-outflow-line incident matrix (BOLIM) and Fig. 2(d) is the bus-inflow-line incident matrix (BILIM).

The row with full zeros in BOLIM corresponds to pure sink bus since there are no outflows from the bus. The row with full zeros in BILIM corresponds to pure source since there are no inflows to the bus. It is clear that bus 4 is a pure sink and bus 1 is a pure source in our example. A system might have more than one pure sink and pure source.

The upstream and downstream tracing sequence is determined based on the two matrices as follows.

**Downstream tracing sequence:**
We start from a pure source (bus 1 for our case). Its outflows will bring generator power forward to their downstream buses. Delete the source bus and its incident lines (This is done by delete the corresponding pure source row 1 and corresponding columns of its incident lines, i.e., columns with element 1 in the row 1 of BOLIM). We have a new graph that is a sub-graph of the original one. Then find the new pure source bus in the current graph and its incident (outflow) lines (Since the current graph still satisfies the conditions of lemma 1, a new pure source bus must be existed.) and repeat the deleting process described above again. This process will continue till there is no line existing in the current graph. Then all the remaining buses are pure sinks. The intermediate BOLIM and BILIM in downstream tracing for our example are shown in Fig. 3.

**Upstream tracing sequence:**
Upstream tracing can be performed similarly through starting from a pure sink. The tracing ends when there is no
To solve the problem we first build up two matrices. One is extraction factor matrix of lines and loads from bus total passing power. The other is contribution factor matrix of generators to bus total passing power. The product of these two matrices constitutes the contribution factors of generators to line flows and loads.

**Extraction factors of lines from bus total passing power**

We first build up an extraction factor matrix \( A_l \) of lines from total passing power of their upstream buses, i.e. \( P_l = A_l \cdot P \). Here \( P_l \) is the vector of line power (\( P_{lb} \) through \( P_{le} \) for our case); \( P \) is the vector of bus total passing power in the bus sequence of downstream tracing and calculated from ac load flow solution. The nonzero element in \( A_l \) is calculated as follows:

\[
(A_l)_{le-bus \ i} = \frac{\text{line } j \text{'s power flow from bus } i \text{ to bus } j}{P_i}
\]

where \( bus \ i \) is the upstream bus of line \( j \). \( P_i \) includes both line inflow power and net generator injection power to bus \( i \) calculated from load flow solution. For our case [see Fig. 2(a)] \( P = (10 86 10)^T \) and \( A_l \) is

\[
A_l = \begin{bmatrix}
3/10 & 0 & 0 & 0 \\
7/10 & 0 & 0 & 0 \\
0 & 2/8 & 0 & 0 \\
0 & 6/8 & 0 & 0 \\
0 & 0 & 0 & 1/6 \\
\end{bmatrix}
\]

Similarly we can form an extraction factor matrix \( (A_L) \) of loads from total passing power of their located buses, i.e. \( P_L = A_L \cdot P \). The vector of load power \( P_L \) takes the same sequence as vector \( P \); therefore matrix \( A_L \) will be a diagonal matrix where

\[
A_{L_{\text{bus}}} = \begin{cases}
0 & \text{if load bus } i \text{ is not in our example array;}
\frac{\text{net load power on bus } i}{P_L} & \text{if bus } i \text{ is in our example array;}
\end{cases}
\]

For our example \( A_L \) will be

\[
A_L = \begin{bmatrix}
P_1 & P_2 & P_3 & P_4 \\
L_1 & 0 & 0 & 0 \\
L_2 & 0 & 0 & 0 \\
L_3 & 0 & 5/6 & 0 \\
L_4 & 0 & 0 & 0 & 10/10 \\
\end{bmatrix}
\]

If we merge matrices \( A_l \) and \( A_L \) together as extraction factor matrix of lines and loads from bus total passing power, we have

\[
A = \begin{bmatrix}
A_l \\
A_L
\end{bmatrix}
\]

can prove that each element in \( A \) is nonnegative; the sum of elements in each column of matrix \( A \) equals one; there is one and only one nonzero element in each row of sub-matrix \( A_l \); and the nonzero elements in sub-matrix \( A_l \) are corresponding to the nonzero elements in BOLIM.

**Contribution factors of generators to bus total passing power**

The downstream tracing (DSTR) is used for calculating the contribution factors of individual generators to line flows and loads. The tracing path is determined by the method suggested in Section II and starting from a pure source. For our example, the DSTR sequence is bus 1 → bus 2 → bus 3 → bus 4. The state variable in DSTR is the net generator power.
We define contribution factor matrix \( B \) of generators to bus total passing power as

\[
P = B \cdot P_G
\]

where the vector of generator power \( P_G(j = 0) \) if bus \( i \) is not a net generator bus) and the vector of bus total passing power \( P \) have same bus sequence as that in downstream tracing. Matrix \( B \) is formed row by row. It is clear from DSTR that for a certain bus \( i \) only its upstream generators have impacts on its total injection (passing) power, so the upper triangular elements of matrix \( B \) corresponding to the downstream generator effects on upstream bus total injection power should be zero. The diagonal elements \( B_{bus\rightarrow bus} \) will be 1 (or 0) if bus \( i \) is (or is not) a net generator bus. A nongenerator bus \( k \) will not have any contribution to the total injection (passing) power of any buses. The elements in matrix \( B \) can be calculated by equation (1):

\[
B_{bus\rightarrow bus} = \begin{cases} 
1 & (k = i, k \in \text{net gen. buses}) \\
0 & (k = i, k \notin \text{net gen. buses}) \\
0 & (k \succ i) \\
0 & (k < i, k \notin \text{net gen. buses}) \\
\sum_{j \in i} (A_{ij}^\dagger - m) \cdot B_{m\rightarrow k} & (k < i, k \in \text{net gen. buses})
\end{cases}
\]

where “\( k < i \)” means \( k \) is an upstream bus of bus \( i \), and “\( k \succ i \)” means \( k \) is a downstream bus of bus \( i \). The last expression is for the lower triangular nonzero elements. The term “\( j \in i \)” means line \( j \) is an inflow line of bus \( i \). \( A_{i \rightarrow m} \) is the unique nonzero element corresponding to line \( j \) in matrix \( A_{i} \) with bus \( m \) as its upstream terminal. \( B_{m\rightarrow k} \) is the element in matrix \( B \) already calculated which represents the contribution of generator \( k \) to the total injection power of bus \( m \). The product \( (A_{i \rightarrow m} \cdot B_{m\rightarrow k}) \) represents the contribution of generator \( k \) to the total injection power of bus \( i \) through line \( j \) (from bus \( m \) to bus \( i \)). For our example it is easy to form matrix \( B \) by row by row according to equation (1). The resultant \( B \) is shown at the bottom of the page.

Now we are ready to determine the contribution factors of individual generators to line flows and loads. Based on matrices \( A_{i}, A_{L} \) and \( B \), since \( P_{i} = A_{i} P \) and \( P = B P_{G} \), so we have

\[
P_{i} = A_{i} P = A_{i} B P_{G} = K_{LG} P_{G}
\]

where \( K_{LG} \) is the contribution factor matrix of generators to line flows. Similarly since \( P_{L} = A_{L} P \) and \( P = B P_{G} \), so we have

\[
P_{L} = A_{L} P = A_{L} B P_{G} = K_{LG} P_{G}
\]

where \( K_{LG} \) is the contribution factor matrix of generators to loads which shows that each load gets their real power supply from which generators and at what contribution factors. For our example we have

\[
K_{LG} = A_{i} B = \begin{bmatrix} 
3/10 & 0 & 0 & 0 \\
7/10 & 0 & 0 & 0 \\
3/40 & 1/4 & 0 & 0 \\
9/40 & 3/4 & 0 & 0 \\
3/80 & 1/8 & 0 & 0 
\end{bmatrix}
\]

and

\[
K_{LG} = A_{L} B = \begin{bmatrix} 
P_{G1} & 0 & 0 & 0 \\
P_{G2} & 0 & 0 & 0 \\
P_{G3} & 0 & 0 & 0 \\
P_{G4} & 3/16 & 5/8 & 0 & 0 \\
3/16 & 3/8 & 0 & 0 
\end{bmatrix}
\]

However we are not able to get \( P_{G} = K_{LG} P_{L} \) and \( P_{L} = K_{LG} P_{L} \) from the downstream tracing since the state variable in DSTR is the generator power. These relations can only be obtained from the upstream tracing which takes the load power as the state variable.

B. Upstream Tracing

The upstream tracing (USTR) is used for calculating the extraction factors of loads from line flows and generators. The tracing path is determined by the method suggested in Section II and beginning from a pure sink bus. For our example, the USTR sequence is bus 4 \( \rightarrow \) bus 3 \( \rightarrow \) bus 2 \( \rightarrow \) bus 1. Usually USTR can takes the reverse sequence of DSTR.

Similarly we first build up two matrices. One is contribution factor matrix of lines to total passing power of their downstream terminal buses. i.e.

\[
P_{L} = A_{L} P = A_{L} B P_{G} = K_{LG} P_{G}
\]
Here $P_i$ and $P$ has the same definition as DSTR but the vector $P$ of total passing power of buses is in the bus sequence of USTR. The nonzero element in $C_l$ is determined as follows:

$$C_l = \begin{bmatrix} P_1 & P_2 & P_3 & P_4 \\ a & 0 & 0 & 3/8 \\ b & 7/10 & 0 & 0 \\ c & 2/10 & 0 & 0 \\ d & 0 & 6/6 & 0 \\ e & 1/10 & 0 & 0 \end{bmatrix}$$

where bus $i$ is the downstream terminal bus of line $j$. For our case $C_l$ is [see Fig. 2(a)]:

$$C_l = \begin{cases} \frac{P_i}{P_i} & \text{if } i \in \text{net gen. buses} \\ 0 & \text{if } i \notin \text{net gen. buses} \end{cases}$$

Similarly we can form a contribution factor matrix $(C_G)$ of generators to the bus total passing power, i.e. $P_G = C_G \bullet P$. If the vector of generator power $P_G$ uses the same USTR sequence as vector $P$, matrix $C_G$ will be a diagonal matrix and

$$C_{G_{i}} = \begin{cases} 0 & \text{if } i \notin \text{net gen. buses} \\ \frac{P_i}{P_i} & \text{if } i \in \text{net gen. buses} \end{cases}$$

For our example $C_G$ will be

$$C_G = \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \\ 0 \end{bmatrix}$$

If we merge matrices $C_l$ and $C_G$ together as contribution matrix $C$ of lines and generators to bus passing power, we have

$$C = \begin{bmatrix} C_l \\ C_G \end{bmatrix}$$

It is easy to prove the following. Each element in $C$ is nonnegative. The sum of elements in each column of matrix $C$ equals one. There is one and only one nonzero element in each row of sub-matrix $C_l$. And the nonzero elements in sub-matrix $C_l$ are corresponding to the nonzero elements in BILIM.

**Extraction factors of loads from bus total passing power**

We define extraction factor matrix $(D)$ of loads from bus total passing power as

$$P = D \bullet P_L$$

where the vector of load power $P_L$ ($P_{Li} = 0$ if bus $i$ is not a net load bus) and the vector of bus total passing power have same bus sequence as that in USTR. Matrix $D$ will be formed row by row. It is clear that for a certain bus $i$ only its downstream loads have impacts on its total outflow (passing) power, so the upper triangular elements of matrix $D$ corresponding to the upstream load effects on downstream bus total outflow (passing) power should be zero. The diagonal element $D_{busi, busi}$ will be 1 (or 0) if bus $i$ is (or is not) a net load bus. A nonload bus $k$ will not have any contribution to the total outflow power of any buses. The elements in matrix $D$ can be calculated by equation (4):

$$D_{busi, busk} = \begin{cases} \frac{1}{\text{net gen. buses}} & \text{if } k = i, \ k \in \text{net load. buses} \\ 0 & \text{if } k = i, \ k \notin \text{net load. buses} \\ 0 & \text{if } k > i \\ 0 & \text{if } k < i, \ k \notin \text{net load buses} \end{cases}$$

where “$k < i$” means bus $k$ is a downstream bus of bus $i$ and “$k > i$” means bus $k$ is an upstream bus of bus $i$. The last expression in equation (4) is for the lower triangular nonzero elements. The term “$l_{ji}$” means that line $j$ is an outflow line of bus $i$. $C_{l_{m-i}}$ is the unique nonzero element corresponding to line $j$ (from bus $i$ to bus $m$) in matrix $C_l$. $D_{m-k}$, is the element in matrix $D$ already calculated which represents the extraction factor of load $k$ from the total passing power of bus $m$. The product $(C_l \cdot l_{ji} \bullet D_{m-k})$ represents the extraction of load $k$ from the total outflow power of bus $i$ through line $j$ (from bus $i$ to bus $m$). For our example it is easy to form matrix $D$ row by row. According to equation (4) the resultant $D$ is

$$D = \begin{bmatrix} 1 & 0 & 0 & 0 \\ \frac{1}{10} \times 1 - \frac{3}{10} \times 1 & 1 & 0 & 0 \\ \frac{3}{10} \times 1 + \frac{3}{10} \times 1 = \frac{3}{10} \times 1 & \frac{3}{10} \times 1 - \frac{3}{10} \times 1 & 1 & 0 & 0 \\ \frac{7}{10} \times 1 + \frac{3}{10} \times 1 = \frac{3}{10} \times 1 & \frac{3}{10} \times 1 - \frac{3}{10} \times 1 & 1 & 0 & 0 \end{bmatrix}$$

Now we are ready to determine the extraction factors of individual loads from line flows and generators. Based on matrices $C_l$, $C_G$ and $D$, since $P_l = C_l P$ and $P = D P_L$, so we have

$$P_l = C_l P = C_l D P_L = K_{IL} P_L$$

where $K_{IL}$ is the extraction factor matrix of loads from line flows. Similarly since $P_G = C_G P$ and $P = D P_L$, so we have

$$P_G = C_G P = C_G D P_L = K_{GL} P_L$$

where $K_{GL}$ is the extraction factor matrix of loads from generators which shows how each generators provides power supply to individual loads. For our example we have

$$K_{IL} = C_l D = \begin{bmatrix} P_{L_1} & P_{L_3} & P_{L_2} & P_{L_1} \\ a & 9/80 & 3/8 & 0 & 0 \\ b & 7/10 & 0 & 0 & 0 \\ c & 2/10 & 0 & 0 & 0 \\ d & 1/10 & 1 & 0 & 0 \\ e & 1/10 & 0 & 0 & 0 \end{bmatrix}$$

and

$$K_{GL} = C_G D = \begin{bmatrix} P_{L_1} & P_{L_3} & P_{L_2} & P_{L_1} \\ P_{C_1} & 0 & 0 & 0 & 0 \\ P_{C_3} & 0 & 0 & 0 & 0 \\ P_{C_2} & 3/16 & 5/8 & 0 & 0 \\ P_{C_1} & 13/16 & 3/8 & 0 & 0 \end{bmatrix}$$
Substituting \( P_L \) in equation (6), or \( P_G \) in equation (3), we have the real power transfer between generators and loads as follows (in MW):

\[
\begin{align*}
P_{G1} &= 10 \quad \rightarrow \quad P_{L3} = 5 \\
P_{G2} &= 5 \quad \rightarrow \quad P_{L4} = 10
\end{align*}
\]

It should be noticed that the sum of elements in each column of matrices \( K_{GL} \) and \( K_{LG} \) equals 1 which means there is a nonindependent equation in matrix equations (3) and (6).

### C. Handling Power Required by Line \( R_e \), \( X \) and \( C \)

A line can be considered as its “\( m \)”-equivalence and its charging capacitance effects can be included in its terminal bus loads according to load flow solution. Line resistance (or reactance) can be treated similarly as follows. If active (or reactive) power flow at the two terminals of the line are in the same direction, the line active (or reactive) power loss can be moved to the sending bus and the line flow will equal to receiving end power flow. The line active (or reactive) power loss can also be moved to the receiving bus, then the line flow will equal to the sending end power flow. However if the two terminals of the line have different active (or reactive) power flow directions, i.e. both terminals inject power to the line, the two injection powers should be treated as equivalent loads on the sending and receiving buses respectively. The resultant line flow will be zero and treated as open circuit. This process is to guarantee that the power flow along the line keeps constant and the line flow has a definite direction.

If loop flows exist in a real power system, the upstream or downstream tracing can detect their existence (i.e. when there is no row with full zero elements in current BOLIM or there is no row with full zero elements in current BILIM).

However to determine the loop flow path and to evaluate the amount of the loop flow are not easy, especially when the loop flows have complicated paths. The issue needs further investigation.

### IV. TEST ON IEEE 30-BUS SYSTEM

In order to test the feasibility and the accuracy of the algorithm proposed above in real world system, computer software has been developed and tested on the IEEE 30-bus system. The active power flow tracing results are shown in Table I, with the system load flow results attached in Appendix.

In Table I, loads on various buses are listed. The power supplies from individual generators to each load are calculated through the suggested power tracing method. Both upstream and downstream tracings yield the same results which are presented in Table I as well. Table I also shows the loss sharing by individual generators and the balance of the system power. The new method is proved to be very simple and quite efficient in power flow tracing.

### TABLE I

#### Analysis of Transferred Power (MW)

<table>
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<tr>
<th>Bus</th>
<th>Load</th>
<th>Supplied by Gen. 1</th>
<th>Supplied by Gen. 2</th>
<th>Supplied by Gen. 5</th>
<th>Supplied by Gen. 8</th>
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### V. Conclusion

In this paper graph theory is applied to calculate the contribution factors of individual generators to line flows and loads and the extraction factors of individual loads from line flows and generators. The power transfer allocation between individual generators and loads are significant to transmission open access. Related lemmas have been proved to guarantee the feasibility of the method. Bus-inflow-line and bus-outflow-line incident matrices are built up to fast determine the downstream and upstream power flow tracing sequences. The DSTR is performed to determine the contribution factors of generations to the line flows and loads, whereas USTR is performed to determine the extraction factors of individual loads from line flows and generators. The suggested method is very efficient and suitable for use in real power systems.
APPENDIX

TABLE II
LOAD FLOW OF THE IEEE 30-BUS SYSTEM

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<th>Voltage Angle (degree)</th>
<th>Generation Active (MW)</th>
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REFERENCES


Felix F. Wu received his Ph.D. degree from University of California at Berkeley (UCB). He is now a Chair Professor of electrical engineering and Vice President of the University of Hong Kong. Prior to that he was a Professor and Vice-Chair of Department of EECS, UCB. His research interests are electric energy industry restructuring, power system investment planning, design of modern control centers, distribution automation, distributed processing etc.

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