Improved Characteristics of InGaZnO Thin-Film Transistor by using Fluorine Implant

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Index Terms

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ABSTRACT

The effects of fluorine incorporation in amorphous InGaZnO by ion implant on the characteristics of InGaZnO thin-film transistor have been investigated. The electrical characteristics of thin-film transistor can be improved by this treatment due to increase of carrier concentration and passivation of defects in InGaZnO. Consequently, the saturation carrier mobility can be increased from 25.2 cm$^2$/V·s to a high value of 34.0 cm$^2$/V·s, and the output current can be nearly doubled. However, device degradation is observed for very high fluorine dose above $1.0 \times 10^{15}$ /cm$^2$, possibly ascribed to excessive interstitial fluorine atoms and too many fluorine-induced electrons in InGaZnO.
Over the years, amorphous InGaZnO (a-IGZO) thin-film transistors (TFTs) have been extensively investigated for applications in various flat-panel displays (FPDs), including electronic papers, organic light-emitting-diode displays (OLEDs) and liquid crystal displays (LCDs).\textsuperscript{1,2} Compared to conventional amorphous-silicon or organic TFTs with a field-effect carrier mobility of $\sim 1$ cm$^2$/V·s,\textsuperscript{3,4} a-IGZO TFTs typically exhibit a mobility higher than 10 cm$^2$/V·s.\textsuperscript{5,6} In addition, a-IGZO TFTs offer better uniformity in device characteristics than polycrystalline silicon TFTs, and have much higher transparency to visible light than all the silicon-based devices.\textsuperscript{7}

In order to meet the requirement of developing advanced display technologies, for example, large-panel LCDs, OLEDs and high-frame-rate three-dimensional (3D) displays, higher carrier motility is required for a-IGZO TFTs. It is well-known that carrier transport in a-IGZO is by percolation conduction, and a potential barrier distribution around the conduction-band bottom is formed due to its disordered structure. If the electron concentration in a-IGZO is increased, the potential barrier will be reduced, resulting in an increase in the carrier mobility of a-IGZO TFTs.\textsuperscript{2,8} Accordingly, doping with other element has been adopted to increase the electron concentration in a-IGZO and thus the carrier mobility of a-IGZO TFTs. For example, high-pressure hydrogen annealing was reported to realize improved electrical characteristics of a-IGZO TFTs, including increased saturation carrier mobility ($\mu_{\text{sat}}$) from 0.7 cm$^2$/V·s to 7.4 cm$^2$/V·s and reduced threshold voltage ($V_{\text{TH}}$) from 5.6 V to 2.2 V, due to the increase of electron concentration in a-IGZO after hydrogen incorporation.\textsuperscript{8} On the other hand, Seo et al. adopted fluorine doping by spin coating of metal-fluoride (ZnF$_2$ and InF$_3$) precursor aqueous solution to improve the carrier mobility and stability of InZnO TFTs.\textsuperscript{9} However, the reported InZnO TFTs had a low $\mu_{\text{sat}}$ of 4.1 cm$^2$/V·s and a high $V_{\text{TH}}$ of 5.5 V even after fluorine incorporation, possibly due to the solution process.

In this work, fluorine doping is introduced to a-IGZO TFTs through ion implant, and accordingly the effects of fluorination on the characteristics of a-IGZO TFTs are investigated both qualitatively and
quantitatively. Furthermore, high-k material has been widely applied as gate dielectric in a-IGZO TFTs in order to reduce their operating voltage.\textsuperscript{10,11} Among them, HfLaO is one of the most promising dielectric materials due to its superior properties, including high dielectric constant, good thermal stability and low trap density,\textsuperscript{12} and thus is selected as the gate dielectric for the a-IGZO TFTs in this work.

The schematic diagrams of the device cross section during fluorine ion implant and after all the fabrication steps are shown in Figure 1 (a) and Figure 1 (b), respectively. In this structure, p-type (100) silicon with a resistivity of 0.01 ~ 0.02 $\Omega \cdot \text{cm}$ acts as both the substrate and gate electrode of each sample. Firstly, the conventional RCA (Radio Corporation of America) method was used to remove the organic and ionic contaminants on the substrate.\textsuperscript{13,14} Secondly, deposition of a 40-nm HfLaO film was done by means of a sputtering system with a radio-frequency (RF) power of 40 W and a metal target of HfLa (with 40\% lanthanum). Moreover, the sputtering process was conducted in a mixed ambient of Ar plus O$_2$ (Ar : O$_2$ = 24 sccm : 6 sccm). Thirdly, an annealing treatment at 400 °C in an ambient of N$_2$ for 10 min with a gas flow rate of 500 ml/min followed in order to improve the quality of the HfLaO film. Subsequently, each sample received a deposition of a 60-nm a-IGZO active layer through RF sputtering from a ceramic target (Ga$_2$O$_3$ : In$_2$O$_3$ : ZnO = 1 : 1 : 1) in an Ar/O$_2$ mixed ambient. Then, some samples were treated by fluorine ion implant at a fixed energy of 20 keV and an angle of 7 degree, with a dose of $3.0 \times 10^{14}$ /cm$^2$, $1.0 \times 10^{15}$ /cm$^2$, $3.0 \times 10^{15}$ /cm$^2$ and $1.0 \times 10^{16}$ /cm$^2$, respectively. In addition, a control sample without fluorine incorporation was also fabricated for comparison purpose. After that, a lift-off process was utilized to form the source/drain electrodes, which were composed of 20-nm Ti and 80-nm Au deposited by means of electron-beam evaporation. Finally, all the samples were annealed in a forming-gas (N$_2$ : H$_2$ = 95 : 5) ambient at 350 °C for 20 min so that the contact resistance of the source/drain electrodes was reduced. In addition, metal-insulator-semiconductor (MIS) capacitor was also prepared beside the transistor in order to monitor the gate-oxide capacitance per unit area ($C_{\text{ox}}$,}
equal to 0.252 µF/cm² in this work). For each TFT device, the channel width (W) and channel length (L) were 100 µm and 30 µm respectively, and all the processing steps except the annealings were conducted at room temperature.

The current-voltage (I-V) curve of the TFTs and the 1-MHz capacitance-voltage (C-V) characteristics of the capacitors were measured by a HP 4145B semiconductor parameter analyzer and a HP 4284A precision LCR meter, respectively. Besides, the physical thicknesses of HfLaO and a-IGZO were measured by a multi-wavelength ellipsometer. All the measurements were conducted in a light-tight, electrically-shielded and room-temperature environment.

Figure 2 exhibits the secondary ion-mass spectrometry (SIMS) depth profile for the a-IGZO/HfLaO/Si stacks implanted with different fluorine doses. The ordinate refers to the atom intensity while the abscissa is about the acquisition time, which reflects the depth from the surface of a-IGZO. As shown in Figure 2, fluorine has been doped in a-IGZO through ion implant, and the fluorine distribution is similar for all the fluorinated samples due to the same implant energy. For each dose, the concentration of fluorine atom is relatively uniform from the surface of a-IGZO to its middle, and then starts to decrease towards the a-IGZO/HfLaO interface.

Figure 3 shows the atomic force microscopy (AFM) results of the a-IGZO/HfLaO/Si stacks implanted with different fluorine doses for a measurement area of 1 µm×1 µm. As shown in Figure 3 (a), the control sample exhibits the roughest a-IGZO surface, with a RMS value of 2.34 nm. With the treatment of fluorine ion implant, the surface roughness of a-IGZO is significantly reduced, which is more obvious for higher dose. Consequently, the RMS value of the fluorinated samples is 1.28 nm, 1.16 nm, 0.58 nm and 0.31 nm for a dose of 3.0×10¹⁴ /cm², 1.0×10¹⁵ /cm², 3.0×10¹⁵ /cm² and 1.0×10¹⁶ /cm², respectively. The correlation between the RMS value and the fluorine dose is exhibited in Figure 3 (f), which shows a strong consistency of the improvement in the surface roughness of a-IGZO induced by fluorine incorporation. This finding proves that fluorine atoms can effectively passivate the defects in a-
IGZO, e.g. by combining with its dangling bonds. Moreover, it is believed that a smoother a-IGZO surface could reveal improved quality of the channel/dielectric interface, which is important for better electrical characteristics of a-IGZO TFTs.

Figure 4 (a) shows the transfer characteristics of the a-IGZO TFTs implanted with different fluorine doses: drain current ($I_D$) vs. gate-to-source voltage ($V_{GS}$), and $I_D^{1/2}$ vs. $V_{GS}$ at a drain-to-source voltage ($V_{DS}$) of 5 V. The electrical parameters of the devices, including $\mu_{sat}$, $V_{TH}$, subthreshold swing (SS) and on-off current ratio ($I_{on}/I_{off}$) are extracted from Figure 4 (a) and listed in Table I. Among them, $\mu_{sat}$ and $V_{TH}$ are calculated from a linear fitting to the plot of $I_D^{1/2}$ versus $V_{GS}$, which is based on the I-V equation of field-effect transistor operating in the saturation region.

$$I_D = (\mu_{sat} C_{ox} W/2L)(V_{GS} - V_{TH})^2$$ (1)

As shown in Figure 4 (b), compared to the control sample ($\mu_{sat} = 25.2 \text{ cm}^2/\text{V}\cdot\text{s}$), $\mu_{sat}$ of the fluorinated sample increases almost linearly with the dose from 0 /cm$^2$ to $1.0\times10^{15}$ /cm$^2$, and then starts to decrease. Accordingly, the highest value of $\mu_{sat}$ (34.0 cm$^2$/V·s) is achieved by the sample with a fluorine dose of $1.0\times10^{15}$ /cm$^2$. It is well known that fluorine and oxygen have similar ionic radius, and the zinc-fluorine chemical bonding (Zn–F) is stronger than the zinc-oxygen chemical bonding (Zn–O). Therefore, it is easy to cause the substitution of the oxygen atoms in the lattice ($O^x$) of a-IGZO by the fluorine atoms derived from the implant. Moreover, the difference in electrovalency between oxygen ion ($O^{2-}$) and fluorine ion ($F^-$) means that the substitution of an oxygen ion by a fluorine ion can generate a free electron:

$$O^x_O + F^- \rightarrow F^+_O + e^-$$ (2)

Therefore, the electron concentration in a-IGZO is increased, resulting in enhanced carrier mobility for a-IGZO TFTs. In addition, the increase of electron concentration in a-IGZO is further revealed by the $V_{TH}$ reduction of the fluorinated samples compared to the control sample in Table I. On the other hand,
the passivation effect of fluorine on the defects in a-IGZO (including both the a-IGZO bulk and the a-
IGZO/HfLaO interface), e.g. the dangling bonds normally acting as electron trap sites in a-IGZO TFTs,
is another important factor for increasing the carrier mobility due to the suppression of trap-induced
scattering on channel electrons. This passivation effect has been supported by the AFM results which
exhibit a continual improvement in the surface roughness of a-IGZO with increasing fluorine dose.
Furthermore, it is believed that the SS of a-IGZO TFTs can be affected by the trap density at/near the a-
IGZO/dielectric interface.1,17 Hence, the reduction of SS after fluorine incorporation, as reflected by the
comparison between the fluorinated sample with a dose of 3.0×10^{14} /cm^2 (SS = 0.29 V/dec) or 1.0×10^{15}
/cm^2 (SS = 0.28 V/dec) and the control sample (SS = 0.32 V/dec), reveals the decrease in the trap
density at/near the a-IGZO/HfLaO interface, further supporting the passivation effect of fluorine on the
defects in a-IGZO. Due to the improvements in \mu_{sat} and V_{TH}, a continual increase in the I_{on}/I_{off} of the a-
IGZO TFT is also achieved as the fluorine dose increases from 0 /cm^2 to 1.0×10^{15} /cm^2. However, the
device performance, including \mu_{sat}, SS and I_{on}/I_{off}, starts to degrade for fluorine dose over 1.0×10^{15} /cm^2,
possibly because excessive fluorine atoms could act as interstitial defects rather than passivators.18 In
addition, for very high fluorine dose, the electron and defect concentrations in the IGZO film can
become high enough to induce significant current flow in its bulk (loss of channel-current modulation by
the V_{GS} sweeping),19 as demonstrated by the hump in the subthreshold region of the transfer curve
shown in Figure 4 (a), and accordingly the SS of TFT increases.

Figure 5 displays the output characteristics of the a-IGZO TFTs implanted with different fluorine
doses. All the samples clearly exhibit n-type enhancement mode. Moreover, I_D increases linearly with
V_{DS} in the region of low V_{DS}, and current saturation can be observed in the region of high V_{DS}. Due to
the improvements in \mu_{sat} and V_{TH}, the output current of the fluorinated sample increases significantly
with the dose from 0 /cm^2 to 1.0×10^{15} /cm^2. As shown in Figure 5(f), under the condition of V_{DS} = 8 V
and V_{GS} = 10 V, the output current of the a-IGZO TFT with a fluorine dose of 1.0×10^{15} /cm^2 (660 \mu A) is
nearly twice that of the control sample (366 µA). On the other hand, performance degradation of the a-IGZO TFT can also be observed from the output characteristics when the fluorine dose is higher than 1.0×10^{15} /cm^2. In addition, since the phenomenon of current crowding is present in Figure 5, the forming-gas annealing needs to be prolonged or done at higher temperature so as to improve the electrical contact between the source/drain electrode and a-IGZO.

In this work, TFT is fabricated on a-IGZO film implanted with fluorine, and accordingly the effects of fluorine incorporation in a-IGZO on the characteristics of a-IGZO TFT have been investigated both qualitatively and quantitatively. It is found that the electrical characteristics of the a-IGZO TFT, including \( \mu_{\text{sat}} \), \( V_{\text{TH}} \), \( I_{\text{on}}/I_{\text{off}} \), SS and output current, can be effectively improved by the fluorine implant due to the dual effects of fluorine incorporation in a-IGZO (increased carrier concentration and defect passivation). Accordingly, increased \( \mu_{\text{sat}} \) (from 25.2 cm^2/V·s to a high value of 34.0 cm^2/V·s) and almost doubled output current are achieved. However, the device performance starts to degrade when the fluorine dose is over 1.0×10^{15} /cm^2, probably due to the formation of fluorine interstitials or excessively high electron concentration in the a-IGZO film heavily doped with fluorine. In summary, a-IGZO fluorination by ion implant is an effective and reproducible method to make high-performance TFT for applications in various advanced FPDs.

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References

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Figure captions

Figure 1. Schematic diagrams of the device cross section: (a) during fluorine ion implant; (b) a-IGZO TFT with HfLaO gate dielectric.

Figure 2. SIMS depth profile for a-IGZO/HfLaO/Si stacks implanted with different fluorine doses.

Figure 3. AFM results of a-IGZO/HfLaO/Si stacks implanted with different fluorine doses: (a) 0 /cm$^2$; (b) 3.0×10$^{14}$ /cm$^2$; (c) 1.0×10$^{15}$ /cm$^2$; (d) 3.0×10$^{15}$ /cm$^2$; (e) 1.0×10$^{16}$ /cm$^2$; (f) RMS value versus fluorine dose.

Figure 4. (a) Transfer characteristics of a-IGZO TFTs implanted with different fluorine doses; (b) $\mu$$_{sat}$ versus fluorine dose.

Figure 5. Output characteristics of a-IGZO TFTs implanted with different fluorine doses: (a) 0 /cm$^2$; (b) 3.0×10$^{14}$ /cm$^2$; (c) 1.0×10$^{15}$ /cm$^2$; (d) 3.0×10$^{15}$ /cm$^2$; (e) 1.0×10$^{16}$ /cm$^2$; (f) I$_D$ @ $V_{DS} = 8$ V and $V_{GS} = 10$ V versus fluorine dose.
Figure 1
Figure 2
Figure 3
Figure 4
Figure 5