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Generalized Series–Parallel RLC Synthesis Without Minimization for Biquadratic Impedances
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Abstract—This brief is concerned with the realization problem of biquadratic impedances, motivated by the recent development in passive mechanical control. This brief generalizes a realization procedure of a special class of biquadratic impedances to a more general form, and the resulting series–parallel RLC networks whose elements are no more than those in Bott–Duffin’s networks are obtained without minimization. The realizability condition is proven by providing a constructive synthesis procedure. The series–parallel network obtained contains elements no more than those of Bott–Duffin’s network when the impedance is nonregular.

Index Terms—Electric circuits, inerter, mechanical networks, network synthesis, passivity.

I. INTRODUCTION

PASSIVE NETWORK synthesis is a classical subject in electrical circuit theory that experienced a “golden era” from the 1930s to the 1970s [1]–[3], [13], but certain aspects are still considered incomplete, such as the question of minimality of realization in terms of the total number of elements. However, interest in passive network synthesis has declined despite relatively recent developments in positive-real functions [9], [12], [20] and the stability of behavioral systems [22]. Recently, a new passive mechanical element, named inerter [23], has been introduced with the property that the (equal and opposite) force applied at the terminals is proportional to the relative acceleration between them. Partially due to the inerter and its applications (see [5] and references therein), interest in passive network synthesis has been revived [4], [6]–[8], [14], [16], [17]. There has been an independent call for a renewed attempt by Kalman [18].

This brief is concerned with the realization problem of biquadratic impedances in series–parallel connections using resistors, inductors, and capacitors (RLC). The biquadratic impedance is one of the most important topics in network synthesis [10], [11], [17], [19], [25] and can provide guidance to the development of the more general cases. Notwithstanding its simple form, many problems are not yet solved, such as minimal realizations. Using the Bott–Duffin method [3], [13], any positive-real biquadratic impedance is realizable by a series–parallel RLC network containing no more than nine elements. Although enumeration [19], [25] has been successfully used to investigate the realization with at most five and six elements, it is much more complex and difficult to apply this method to networks containing more than six elements, and the results would have limited guidance to impedances of higher degrees. Hence, investigation into some more general realization procedures is needed, such as the development of realization procedure in [24]. In [24], an alternative realization procedure for biquadratic impedances is proposed and the resulting network does not contain more elements than Bott–Duffin’s result. However, the biquadratic impedances investigated in [24] are assumed to contain multiple real zeros (poles) and arbitrary poles (zeros). Hence, a generalization of this procedure to a more general class of biquadratic impedances is required.

Section II presents the main results. Section II-A investigates the realization problem of the class of biquadratic impedances with real zeros (poles) and arbitrary poles (zeros). Section II-B further generalizes the class to arbitrary biquadratic impedances. Conclusions are drawn in Section III.

II. MAIN RESULTS

A. Biquadratic Impedances With Real Zeros and Arbitrary Poles

We focus our attention on a biquadratic impedance

\[
Z(s) = \frac{A s^2 + B s + C}{D s^2 + E s + F}
\]

where \(A, B, C, D, E, F \geq 0\). We first present a few lemmas before establishing our main results.

Lemma 1: [8] Consider a biquadratic impedance \(Z(s)\) in the form of (1) where \(A, B, C, D, E, F \geq 0\). Then, \(Z(s)\) is positive-real if and only if \((\sqrt{AF} - \sqrt{CD})^2 \leq BE\).

Lemma 2: [15] Consider a positive-real biquadratic impedance \(Z(s)\) in the form of (1), where \(A, B, C, D, E, F \geq 0\). If any of the coefficients is zero, then \(Z(s)\) can be realized with at most four elements.

Although the next lemma can be easily extended from a result in [15], we provide an alternative proof as follows in order to facilitate our investigation.

Lemma 3: Consider a biquadratic impedance \(Z(s)\) in the form of (1) where \(A, C, D, F > 0\) and \(B, E \geq 0\). If \(AF = CD\), then \(Z(s)\) can be realized by a series–parallel network containing at most four elements. If, in addition, either \(B\) or \(E\) is zero, then the number of elements is at most three; if, in addition, \(AE = BD\), then \(Z(s)\) is realizable as a resistor.
Proving that $A,B,C,D,E,F \Rightarrow D$ is proven.

From Lemma 2 and Lemma 3, we can assume that $A,B,C,D,E,F \Rightarrow D$ to investigate other realizations.

**Lemma 4:** [2, Corollary 3.1] Consider a strictly Hurwitz polynomial $Q(s)$. Let $Q(s) = m(s) + n(s)$, where $m(s)$ and $n(s)$ are even and odd parts of $Q(s)$, respectively. Then, $Z(s) = m(s)/n(s)$ must be a reactance function.

The next lemma has been stated without a detailed proof in [11] and [24]. We restate it here with a brief proof.

**Lemma 5:** [11], [24] Consider a biquadratic impedance $Z(s)$ in the form of (1) where $A,B,C,D,E,F \Rightarrow D$. If $|AF - CD| \leq BE$, then $Z(s)$ can be realized by a series–parallel network with at most seven elements.

**Proof:** Suppose that $AF < CD$, then decompose $Z(s)$ as $Z(s) = (A/D) \cdot m_2/(m_2 + n_2) + (A/D) \cdot m_1/n_1$, where $m_1 = s^2 + C/A$, $n_1 = B/s/A$, $m_2 = s^2 + F/D$, and $n_2 = E/s/D$. Then, $Z(s)$ is realizable as shown in Fig. 2(a) and by removing the pole of $Z_3(s)\Rightarrow D$ at $s = \infty$. When $AF > CD$, by duality, we obtain a network as in Fig. 2(b), where $N_3$ is the dual network of $N_1$.

It has been shown in [24] that, for any impedance of this class not satisfying the condition of Lemma 5, a positive $q$ can always be found such that $Z(s) = H(s^2 + z_2^2)/s^2 + b_1 s + b_0$ or $Z(s) = H(s^2 + a s + a_0)/(s + p)^2$ can be realized as series–parallel networks with at most nine elements by simultaneous multiplication of $(s + q)$ when the condition of Lemma 5 does not hold. In this subsection, we generalize this class to the biquadratic impedances with real zeros (poles) and arbitrary poles (zeros). By duality, it suffices to focus on

$$Z(s) = H(s^2 + z_2^2)/(s^2 + b_1 s + b_0)$$

where $H$, $b_0$, $b_1 > 0$, $z_1 \geq z_2 > 0$, and $z_1 z_2 \neq b_0$. Moreover, by the frequency transformation $s \leftrightarrow s^{-1}$, it can be assumed that $z_1 z_2 > b_0$. Since $Z(s)$ should be positive-real and we assume that it does not satisfy the condition of Lemma 5, the coefficients should further satisfy

$$
\left(\frac{\sqrt{z_1 z_2} - \sqrt{b_0}}{z_1 + z_2}\right)^2 \leq b_1 < \frac{z_1 z_2 - b_0}{z_1 + z_2}.
$$

(3)

Obviously, $Z(s)$ is a minimum function if and only if $b_1 = \left(\frac{\sqrt{z_1 z_2} - \sqrt{b_0}}{z_1 + z_2}\right)^2/(z_1 z_2)$.

The following theorem presents a constructive procedure, different from the Bott–Duffin synthesis, to synthesize a biquadratic impedance $Z(s)$ in the form of (2).

**Theorem 1:** Consider a biquadratic impedance $Z(s)$ in the form of (2), where $H, b_0, b_1 > 0, z_2 \geq z_1 > 0, z_1 z_2 > b_0$ and (3) are satisfied. Then, through a constructive procedure, $Z(s)$ can be realized by a series–parallel network containing at most nine elements. In particular, if $Z(s)$ is a minimum function, then the number of elements is at most eight.

**Proof:** This theorem is proven by presenting the synthesis procedure. First, multiplying the numerator and denominator with a common factor $(s + q)$ with $q > 0$, $Z(s)$ is obtained as

$$
Z(s) = H(s^2 + z_1 z_2 s + z_1 z_2 + (s + q)/(s + q)) = H(m_1 n_1/m_2 n_2),
$$

where $m_1 = (q + z_1 z_2 s + z_1 z_2 + (s + q)/(s + q)), n_2 = s^2 + b_1 s + b_0$, and $m_1 = (q + b_1 s + b_0)$. Then, decompose $Z(s)$ as $Z(s) = Z_1(s) + Z_2(s)$, where $Z_1(s) = H(m_2 n_2) + Z_3(s)$, and $Z_2(s) = H(m_2 n_2)/(m_2 n_2)$. Now, $Z_3(s)$ is a strictly Hurwitz polynomial, and we know $m_2/n_2$ must be a reactance by Lemma 4. Furthermore, so $n_2 = (H/m_2)$, and it can be realized by a lossless network containing two inductors and one capacitor. Therefore, $Z_1(s) = 1/(H/m_2)$ is realizable as the network with one resistor in parallel with a lossless subnetwork containing three reactive elements. For $Z_2(s)$, removal of the pole of $Z_2(s)$ at infinity yields $Z_2^{-1}(s) = s(H(q + s z_2 + z_2))/s^2$, where $Z_3(s)$ is a biquadratic function in the form of

$$
Z_3(s) = \alpha s^2 + \alpha_1 s + \alpha_0
$$

with $\alpha_0 = H(q + z_1^2 z_2^2), \alpha_1 = H((z_1 + z_2 - b_1 q + (z_1 z_2 - b_0))/(s + p)^2$.\quad (4)$

$$
\gamma(b_1) = \gamma_1 - \gamma_2
$$

where $b_1 = \gamma_1 - \gamma_2$. The following quadratic equation in $q$:

$$(z_1 z_2 - b_0)q^2 + 2(z_1 z_2 - b_0 - 1)q + (z_1 z_2 - b_0)q + (z_1 z_2 - b_0)q + (z_1 z_2 - b_0)q + (z_1 z_2 - b_0)q + (z_1 z_2 - b_0)q + (z_1 z_2 - b_0)q = 0.$$
suggesting that (5) has one and only one positive root \( q_0 \) together with the assumption that \( z_{1,2} > b_0 \).

It is not difficult to see that \( \alpha_2, \alpha_0, \beta_0 \geq 0 \) when \( q = q_0 \). In addition, we have the following relations: \( b_1 < (z_{1,2} - b_0)/(z_{1,2}) \Leftrightarrow z_{1,2} - b_1 > (b_1 z_1 + b_1 z_2 + b_0 - z_{1,2}) \). Hence, we can see \( \alpha_1 > 0 \) when \( q = q_0 \). It is seen that \( \beta_0 = 0 \) in \( q \) has one and only one positive root \( q = q_1 \) as \( b_1 z_1 + b_1 z_2 + b_0 - z_{1,2} < 0 \). To show that \( \beta_2 > 0 \), it suffices to show that \( q_1 < q_0 \). Substituting \( q_1^2 + 2b_1 q_1 + (b_1 z_1 + b_1 z_2 + b_0 - z_{1,2}) = 0 \) into the left-hand side of (5), i.e., \( W(q) := (z_{1,2} - b_0) q_1^2 + 2(z_{1,2} b_1 - b_0 z_1 - b_0 z_2) q_1 + (z_{1,2} z_1^2 + z_{1,2} z_2^2 - b_1 z_1 (z_{1,2} + z_2^2) + b_0 (z_1 + z_2) z_2 + (z_{1,2} + b_0 z_2)^2) \), we can obtain that \( W(q_1) = 2b_0 (b_1 - z_{1,2}) q_1 + b_0 (b_1 z_1 + b_1 z_2 + b_0 - z_{1,2} - (z_{1,2} - b_0 z_2)^2) < 0 \), which shows that \( q_1 < q_0 \).

Now, it remains to show that \( \beta_1 \geq 0 \) when \( q = q_0 \). If we regard \( \beta_1 \) as a quadratic function of \( q \) with \( q > 0 \), then the discriminant of \( \beta_1 \) can be calculated as \( \delta(q) = (b_1 z_1 + b_1 z_2 + b_0 - z_{1,2})^2 - 4b_1 b_0 (z_{1,2} + z_1) ((z_{1,2} + z_1) s + b_1 z_1 (z_{1,2} + z_2) + b_0 (z_1 + z_2) z_2 + (z_{1,2} + b_0 z_2)^2) \). Hence, the two roots of \( \delta(q) = 0 \) are solved as \( \beta_1^{(1)}(q) = (\sqrt{z_{1,2} - \sqrt{b_0}})/(z_{1,2} + z_1) \) and \( \beta_1^{(2)}(q) = (\sqrt{z_{1,2} + \sqrt{b_0}})/(z_{1,2} + z_1) \). It is observed that \( \beta_1^{(1)} = x_1 \) and \( \beta_1^{(2)} - x_2 = (2\sqrt{\sqrt{z_{1,2} + b_0}})/(z_{1,2} + z_1) > 0 \). Hence, we can see \( \delta(q) < 0 \) when \( x_1 < b_1 < x_2 \) and \( \delta(q) = 0 \) when \( b_1 = x_1 \). When \( x_1 < b_1 < x_2 \), it follows that \( \beta_1 > 0 \) when \( q = q_0 \). Thus, by Lemma 3, \( Z_3(s) \) is realizable by the network in Fig. 1 (some elements may be short or open circuited), which contains at most four elements.

When \( b_1 = x_1 \), i.e., \( Z(s) \) is a minimum function, it is seen that the quadratic equation \( \beta_1 = 0 \) in \( q \) has a common positive root, which is \( q_0 = (z_{1,2} - (z_{1,2} + z_1) b_1 - b_0)/(2b_1) = (\sqrt{b_0} z_{1,2} + z_1) \). It is noted that \( q_0 = q_0(q) \) is satisfied when \( q = q_0 \) and \( b_1 = x_1 \). This means that \( q_0 = q_0(q) \) when \( b_1 = x_1 \). Hence, \( \beta_1 = 0 \) when \( q = q_0 \) and \( b_1 = x_1 \). By Lemma 3, \( Z_3(s) \) is realizable with at most three elements. Then, the proof is completed.

The realization procedure in Theorem 1 is summarized as follows.

1) Calculate the positive root \( q_0 \) from (5) in \( q \), which is proven to contain one and only one positive root. This guarantees the solution of this procedure to be unique.

2) Multiply the numerator and denominator of \( Z(s) \) with the factor \((s + q_0)\) simultaneously and express it as \( Z(s) = H(m_1 + n_1)/(m_2 + n_2) \). This operation makes the realization possible at the cost of the complexity of the configurations since the degrees of numerator and denominator both increase by one.

3) Decompose \( Z(s) \) as \( Z(s) = Z_1(s) + Z_2(s) \), where \( Z_1(s) = H n_2/(m_2 + n_2) \) and \( Z_2(s) = H (m_1 + n_1 - n_2)/(m_2 + n_2) \).

Then, it is seen that the final realization must contain two subnetworks in a series, whose impedances are \( Z_1(s) \) and \( Z_2(s) \), respectively.

4) Since we can express \( Z_1(s) \) in the form of \( Z_1(s) = (1/H + m_2/(H n_2))^{-1} \), it can obviously be direct realization as a resistor in parallel with a lossless subnetwork whose admittance is \( m_2/H n_2 \). In addition, \( m_2/H n_2 \) is realizable with two inductors and one capacitor in Foster’s second form [2, p. 49].

5) Removing the pole of \( Z_2(s) \) at \( s = \infty \), it follows that \( Z_2(s) \) is realizable as a capacitor in parallel with a subnetwork \( N \) whose impedance is \( Z_3(s) \). The proof in Theorem 1 guarantees that \( Z_3(s) \) can always be realized as in Fig. 1 and the method in the proof of Lemma 3 is used. The final network is a series connection of the realizations of \( Z_1(s) \) and \( Z_2(s) \), which is a series–parallel network as shown in Fig. 3(a).

It is observed that \( \beta_1 \) is nonregular. Since any regular biquadratic impedance \( Z(s) \) is realizable with at most five elements [15] by the Foster preamble, it is only necessary to assume that \( Z(s) \) is nonregular for the statement of the next theorem.

Theorem 2: Consider a nonregular positive-real biquadratic impedance \( Z(s) \) in the form of (1) with \( A, B, C, D, E, F > 0 \). If \( B^2 - 4AC \geq 0 \) or \( E^2 - 4DF \geq 0 \), then \( Z(s) \) can be realized by the network shown in Fig. 2 through the method in the proof of Lemma 5 or by the network shown in Fig. 3 using the procedure outlined in Theorem 1 (or its dual or \( s \leftrightarrow s^{-1} \)). Furthermore, if the networks are to be realized in series–parallel connections, then it contains elements no more than those of Bott–Duffin’s network.

Proof: Since \( Z(s) \) is nonregular, it is known from [15, Lemma 6] that \( AF \neq CD \).

Based on Lemma 5, we know that \( Z(s) \) can be realized by the network shown in Fig. 2 through the method in the proof of Lemma 5 if \( AF - CD ) \leq BE \).

Therefore, we assume that the condition of Lemma 5 does not hold. If \( B^2 - 4AC \geq 0 \) and \( AF < CD \), then \( Z(s) \) can be written in the form of (2), where \( H, b_0, b_1 > 0 \), \( z_{1,2} > b_0 \), and (3) are satisfied. Through the method in Theorem 1 (outlined right after its proof), the series–parallel network shown in Fig. 3(a) containing at most nine elements is derived. In particular, the number of elements is at most eight when \( Z(s) \) is a minimum function. Since the Bott–Duffin method can realize a nonregular positive-real biquadratic impedance with at most nine elements when the network is in series–parallel connections (the number is eight for a minimum function), the realization contains elements no more than those of Bott–Duffin’s network. By duality and the transformation \( s \leftrightarrow s^{-1} \), the other cases can also be proven.

Although the number of elements would be reduced by one through Pantell’s synthesis [21], the configurations obtained would be bridge networks instead of series–parallel networks. In practice, the series–parallel mechanical networks have its own advantages than the bridge ones, such as requiring less space, ease of implementation, better durability, etc. Hence, we only consider series–parallel realizations in this brief.
B. Further Generalization to General Biquadratic Impedances

In this section, we further generalize the class to arbitrary biquadratic impedances. By duality and the frequency transformation \( s \leftrightarrow s^{-1} \), we only need to concentrate on \( Z(s) \) in the form of

\[
Z(s) = H \frac{s^2 + a_1 s + a_0}{s^2 + b_1 s + b_0}
\]

(6)

where \( H, a_1, a_0, b_1, b_0 > 0 \) and \( a_0 > b_0 \). Since \( Z(s) \) should be positive-real and we assume that it does not satisfy the condition of Lemma 5, the coefficients should further satisfy

\[
\left( \frac{\sqrt{a_0} - \sqrt{b_0}}{a_1} \right)^2 \leq b_1 < \frac{a_0 - b_0}{a_1}.
\]

(7)

The next theorem presents the realization of this class.

**Theorem 3:** Consider a biquadratic impedance \( Z(s) \) in the form of (6) where \( H, a_1, a_0, b_1, b_0 > 0 \) and (7) are satisfied. Then, the realization procedure in the proof of Theorem 1 can be generalized to synthesize \( Z(s) \) and the realization is the series–parallel network shown in Fig. 3(a) with at most nine elements. In particular, if \( Z(s) \) is a minimum function, then the number of elements is at most eight.

**Proof:** In the proof of this theorem, we can assume that \( a_1^2 - 4a_0 < 0 \), i.e., the zeros are complex, since the case of \( a_1^2 - 4a_0 \geq 0 \) has been discussed in Theorem 1. Multiplying the numerator and denominator of \( Z(s) \) by a common factor \((s + q) > 0\), we obtain

\[
Z(s) = H \frac{s^3 + (q + a_1)s^2 + (a_1q + a_0)s + a_0q}{s^3 + (q + b_1)s^2 + (b_1q + b_0)s + b_0q} =: H \frac{m_1 + n_1}{m_2 + n_2}
\]

where \( m_1 = (q + a_1)s^2 + a_0q, n_1 = s^3 + (a_1q + a_0)s, m_2 = (q + b_1)s^2 + b_0q, \) and \( n_2 = s^3 + (b_1q + b_0)s + b_0q \). Decompose \( Z(s) \) as \( Z(s) = Z_1(s) + Z_2(s) \), where \( Z_1(s) = H \frac{m_1}{m_2 + n_2} = H \frac{s^3 + (q + a_1)s^2 + (a_1q + a_0)s}{(s^3 + (q + b_1)s^2 + (b_1q + b_0)s + b_0q)} \) and \( Z_2(s) = H \frac{n_1}{m_2 + n_2} = H \frac{(q + a_1)s^2 + (a_1q + b_0)s + a_0q}{(s^3 + (q + b_1)s^2 + (b_1q + b_0)s + b_0q)} \). As discussed in the proof of Theorem 1, \( Z_1(s) \) is realizable as a resistor in parallel with a lossless subnetwork consisting of two inductors and one capacitor, which comprises the left part of the network shown in Fig. 3(a). Removal of the pole of \( Z_2^{-1}(s) \) at infinity gives \( Z_2^{-1}(s) = s/(H(q + a_1) + Z_3^{-1}(s)) \), where \( Z_3(s) \) is in the form of (4) with \( \alpha_2 = H(q + a_1), \alpha_1 = H((a_1 - b_1)q + (a_0 - b_0))(q + a_1), a_0 = Hb0q(q + a_1), \beta_2 = q^2 + 2b_1q + (a_1b_1 + b_0 - a_0), \beta_1 = b_1q^2 + (a_1b_1 + b_0 - a_0)q + a_1b_0, \) and \( \beta_0 = b_0q(q + a_1) \). Let \( \alpha_2\beta_2 = \alpha_0\beta_0 = 0 \); then, we obtain the following equation in \( q \):

\[
(a_0 - b_0)q^2 + 2(a_0b_1 - a_1b_0)q + (a_0a_1b_1 + a_0b_0 - a_0^2 - a_1^2b_0) = 0.
\]

(8)

Using the similar method as the proof in Theorem 1, it can be proven that (8) has one and only one positive root \( q_0 \) because of \( a_0 > b_0 \) and (7). It is obvious that \( \alpha_2, \alpha_0, \beta_0 > 0 \) when \( q = q_0 \). The equation \( \beta_2 = 0 \) in (8) has \( q \) in one and only one positive root because of \( a_1b_1 + b_0 - a_0 < 0 \). To show \( \beta_2 > 0 \), it suffices to show that \( q_1 < q_0 \), where \( q_1 \) is the unique positive root of \( \beta_2 = 0 \) in \( q \). Substituting \( q_1^2 + 2b_1q_1 + (a_1b_1 + b_0 - a_0)q_1 + (a_0a_1b_1 + a_0b_0 - a_0^2 - a_1^2b_0) = 0 \), one can conclude that \( Z(s) \) is realizable as a series–parallel network.
Proof: Considering Lemma 5 and Theorem 3, this theorem follows immediately through duality and the frequency transformation $s \leftrightarrow s^{-1}$.

We now use the $U$–$V$ plane used in [17] to graphically compare the regions of conditions. Express $Z(s)$ in its canonical form as discussed in [17], i.e., $Z_c(s) = (s^2 + 2UVWS + W)/(s^2 + (2V/W)s + 1/W)$, where $U, V, W > 0$. For any rational function $\rho(U, V, W)$, we introduce the notations $\rho'(U, V, W) = \rho(U, V, W^{-1})$ and $\rho^+(U, V, W) = \rho(V, U, W)$. Let $\lambda_0 = 4UV - 4V^2W - (1/W - W)$, $\gamma_0 = 4UV - (W - 1/W)$, and $\sigma_c = 4UV - (1/W + W - 2)$. It is known in [17] that $Z(s)$ is positive-real if and only if $\sigma_c \geq 0$; the condition of Lemma 5 is satisfied if and only if $\gamma_c \geq 0$ when $W > 1$ and $\gamma_c \geq 0$ when $W \leq 1$; $Z(s)$ is regular if and only if $\lambda_c \geq 0$ or $\lambda_c^+ \geq 0$ when $W \leq 1$ and $\lambda_c \geq 0$ or $\lambda_c^+ \geq 0$ when $W > 1$. Then, the conditions of using realization procedures can be expressed as a $U$–$V$ plane when $W = 3$, which is as shown in Fig. 4. It is seen that the positive-real region is divided into Region I, Region II, and Region III, which correspond to the condition of Theorem 3, the condition of Lemma 5 and being nonregular, and the condition of being regular, respectively. The condition of the work by Tirtoprodjo only corresponded to the line segments in Region I. It is noted that this brief enlarges the case from those line segments to the entire Region I, which, together with Region II and Region III, forms the whole positive-real region.

III. Conclusion

This brief has discussed the realization of biquadratic impedances, motivated by the recent development in passive mechanical control. Tirtoprodjo [24] had proposed a procedure to realize the class of the biquadratic impedances with multiple real zeros (poles) and arbitrary poles (zeros). This brief has extended Tirtoprodjo’s synthesis to realize arbitrary positive-real biquadratic impedances by providing a constructive synthesis procedure. The series–parallel network obtained contains elements no more than those in Bott–Duffin’s network when the impedance is nonregular.

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